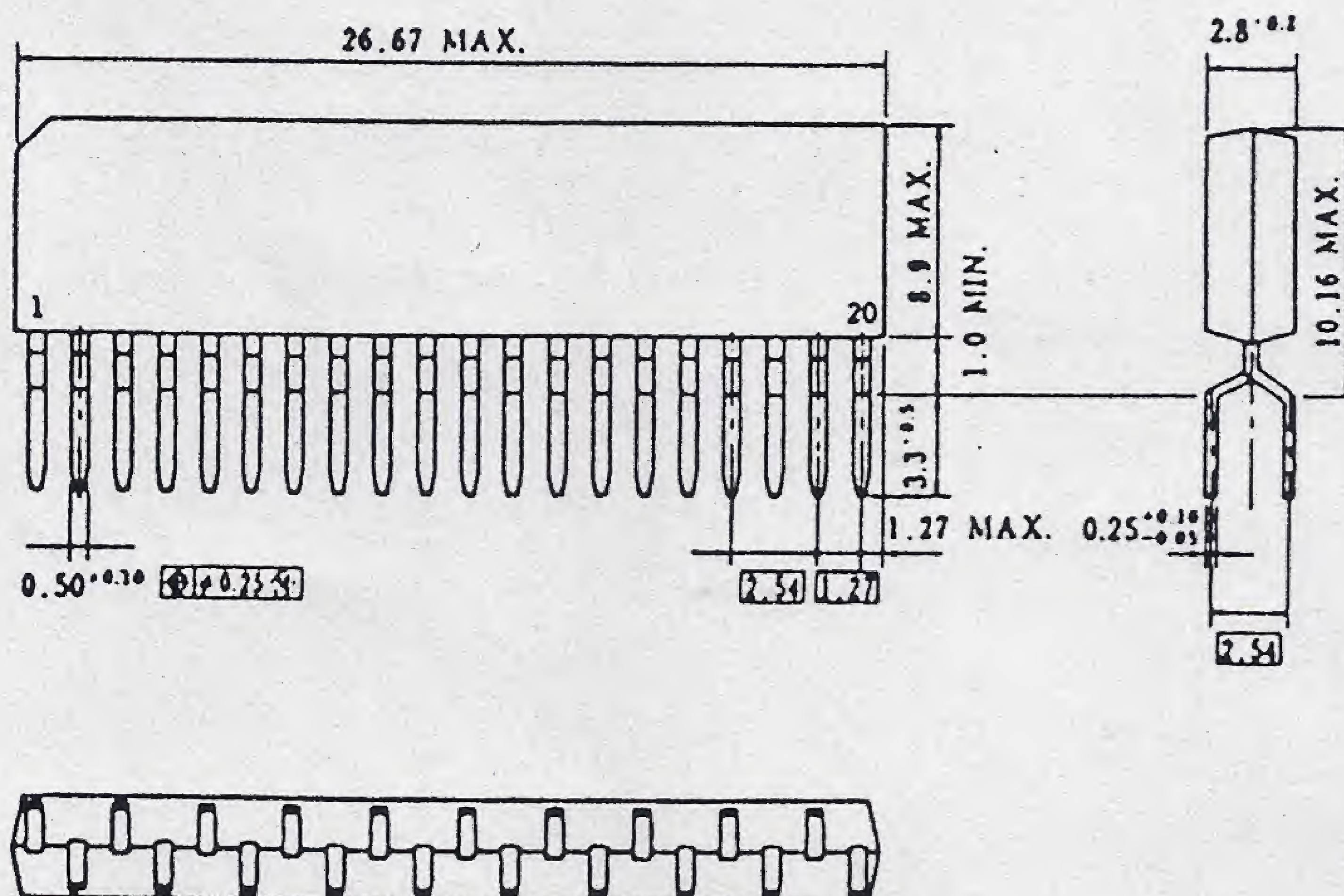


PIN ASSIGNMENTS

A ₀	12	33	CAS
D _{OUT}	13	34	CND
D _{IN}	14	35	WE
RAS	15	36	NC
NC	16	37	NC
A ₆	17	38	A ₁
A ₅	18	39	A ₂
V _{CC}	19	40	A ₃
A ₄	20	41	A ₄
A ₇	21	42	A ₅

PACKAGING CONFIGURATION



ATARI CORPORATION

SIZE
A

DRAWING NO.

C103641-xxx

REV

A

SCALE NONE

SHEET 2 OF 2

THIRD ANGLE SYSTEM DIMENSION mm


REV	REVISIONS DESCRIPTION	DATE	APPROVED
A	PRODUCTION RELEASE	10-24-89	<i>[Signature]</i>

IC DRAM CMOS 1M x 1 ZIP

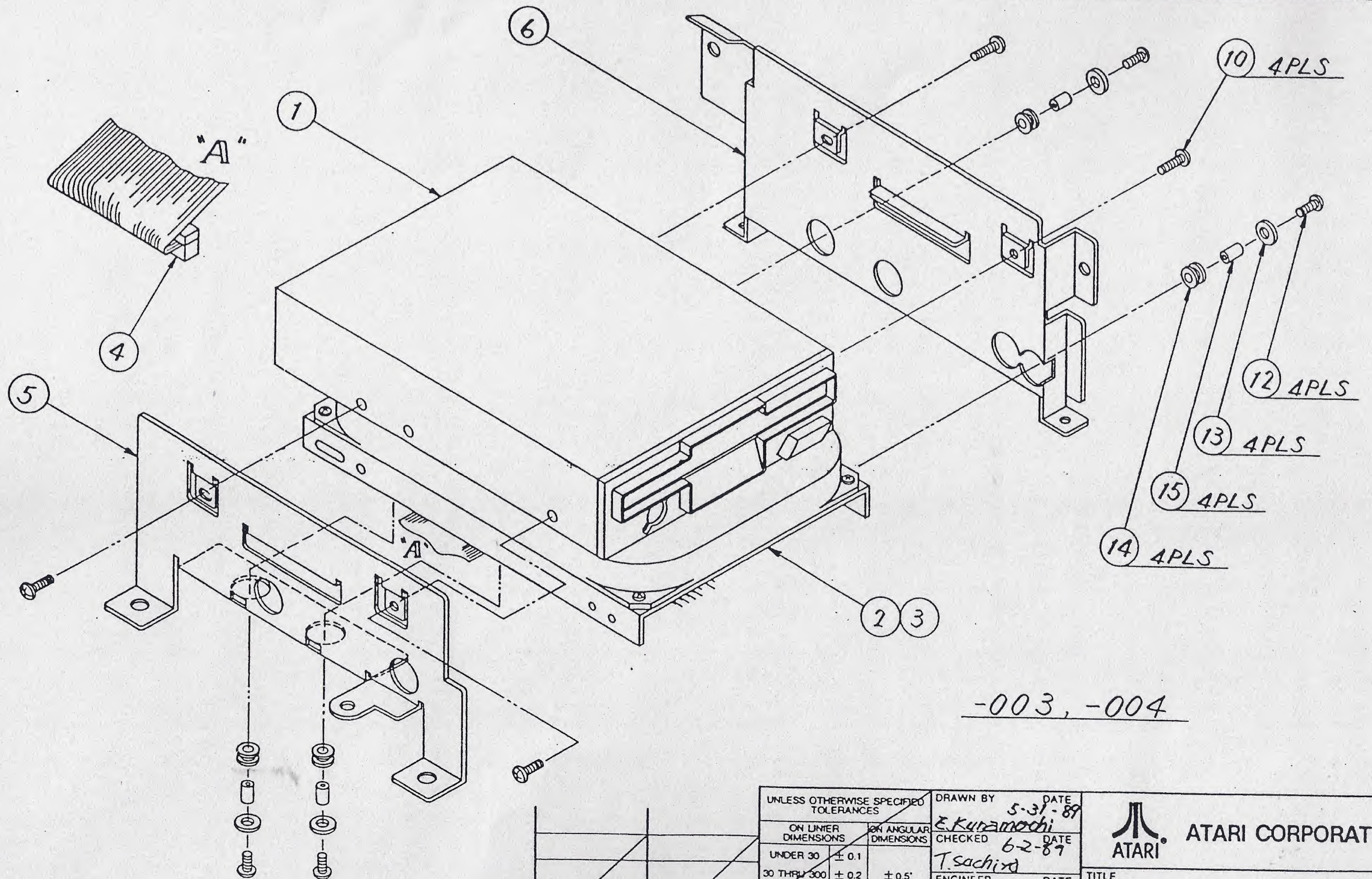
PART NO.	APPROVED VENDOR	VENDOR PART NO.	ACCESS TIME
C103641-001	NEC	μPD421000V-12	120 nsec
	TOSHIBA	TC511000Z-12	
	FUJITSU	MB81C1000-12	
C103641-002	NEC	μPD421000V-10	100 nsec
	TOSHIBA	TC511000Z-10	
	FUJITSU	MB81C1000-10	
C103641-003	NEC	μPD421000V-85	80 nsec
	TOSHIBA	TC511000Z-85	85 nsec
	FUJITSU	MB81C1000-85	

1. THIS DRAWING SHOWS PIN ASSIGNMENT AND PACKAGING CONFIGURATION FOR ZIP PACKAGE ONLY.
 REFER TO DWG NO. C101712-XXX (DIP PACKAGE) FOR OTHER SPECIFICATIONS.

NOTES - UNLESS OTHERWISE SPECIFIED

CA200527-xxx CA200528-xxx LST NEXT ASSY USED ON ORIGINAL MODEL		DRAWN BY <i>E. Kuramochi</i> DATE <i>6-2-89</i>		 ATARI CORPORATION	
		CHECKED <i>T. Tsuchiya</i> DATE <i>6-3-89</i>			
		ENGINEER <i>A. Shinagawa</i> DATE <i>6-3-89</i>		TITLE IC DRAM CMOS 1M x 1 ZIP	
		APPROVED _____ DATE _____		SIZE A	DRAWING NO. C103641-xxx
APPLICATION		APPROVED <i>S. OKAMURA</i> DATE <i>6/3/89</i>		SCALE NONE	
				SHEET 1 OF 2	

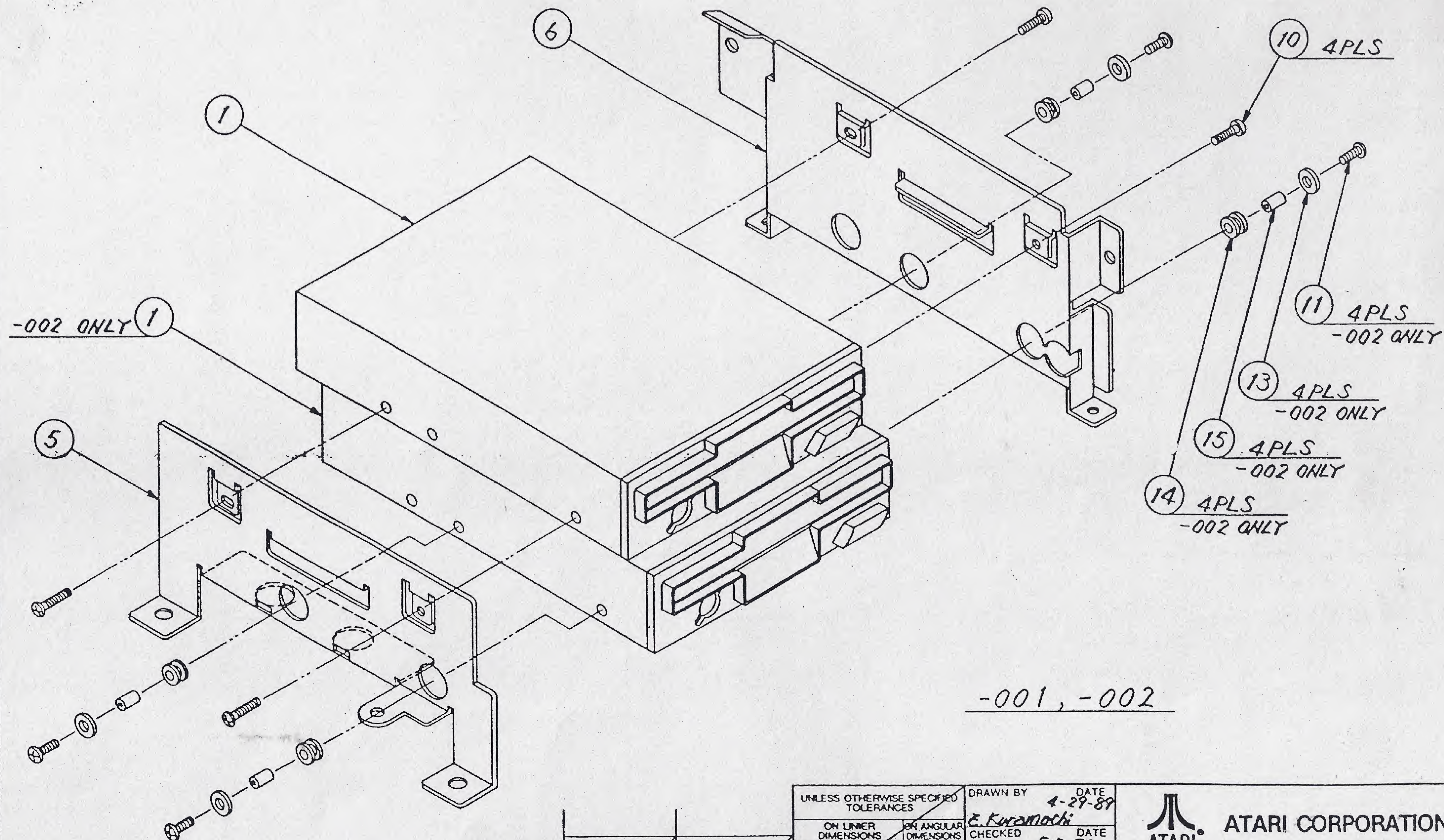
REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



-003, -004

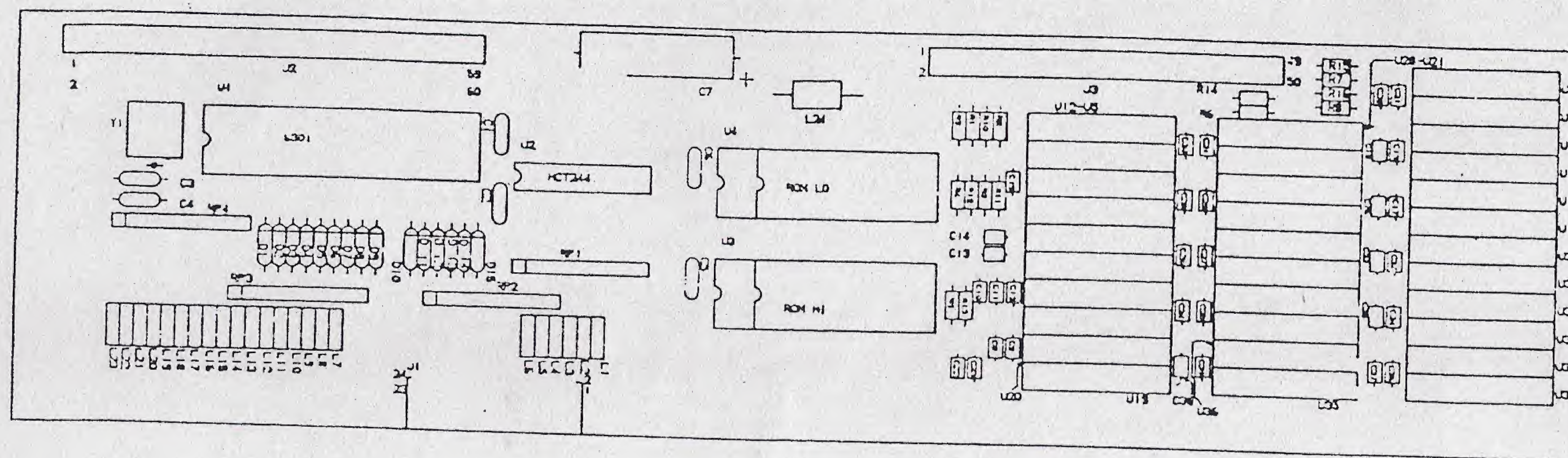
UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY E. Kuramochi DATE 5-31-89		ATARI CORPORATION	
ON LINEAR DIMENSIONS	ON ANGULAR DIMENSIONS	CHECKED T. Sachin DATE 6-2-89	TITLE ASSY DRIVE LST		
UNDER 30	± 0.1	ENGINEER	DATE 6-3-89	SIZE B	DRAWING NO. CA200470-xxx
30 THRU 300	± 0.2	APPROVED JLLS DATE 6/3/89		REV B	
OVER 300	± 0.4				
MATERIAL					
USED ON					

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY <i>E. Kuramochi</i> DATE <i>4-29-89</i>		ATARI CORPORATION	
ON LINER DIMENSIONS	ON ANGULAR DIMENSIONS	CHECKED <i>T. Tsuchiya</i> DATE <i>5-3-89</i>	ENGINEER		
UNDER 30	± 0.1	± 0.5°		TITLE <i>ASSY DRIVE LST</i>	
30 THRU 300	± 0.2				SIZE <i>R</i> DRAWING NO. <i>CA200A70-XXX</i>
OVER 300	± 0.4			REV <i>D</i>	
MATERIAL		APPROVED <i>III</i> DATE			

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		

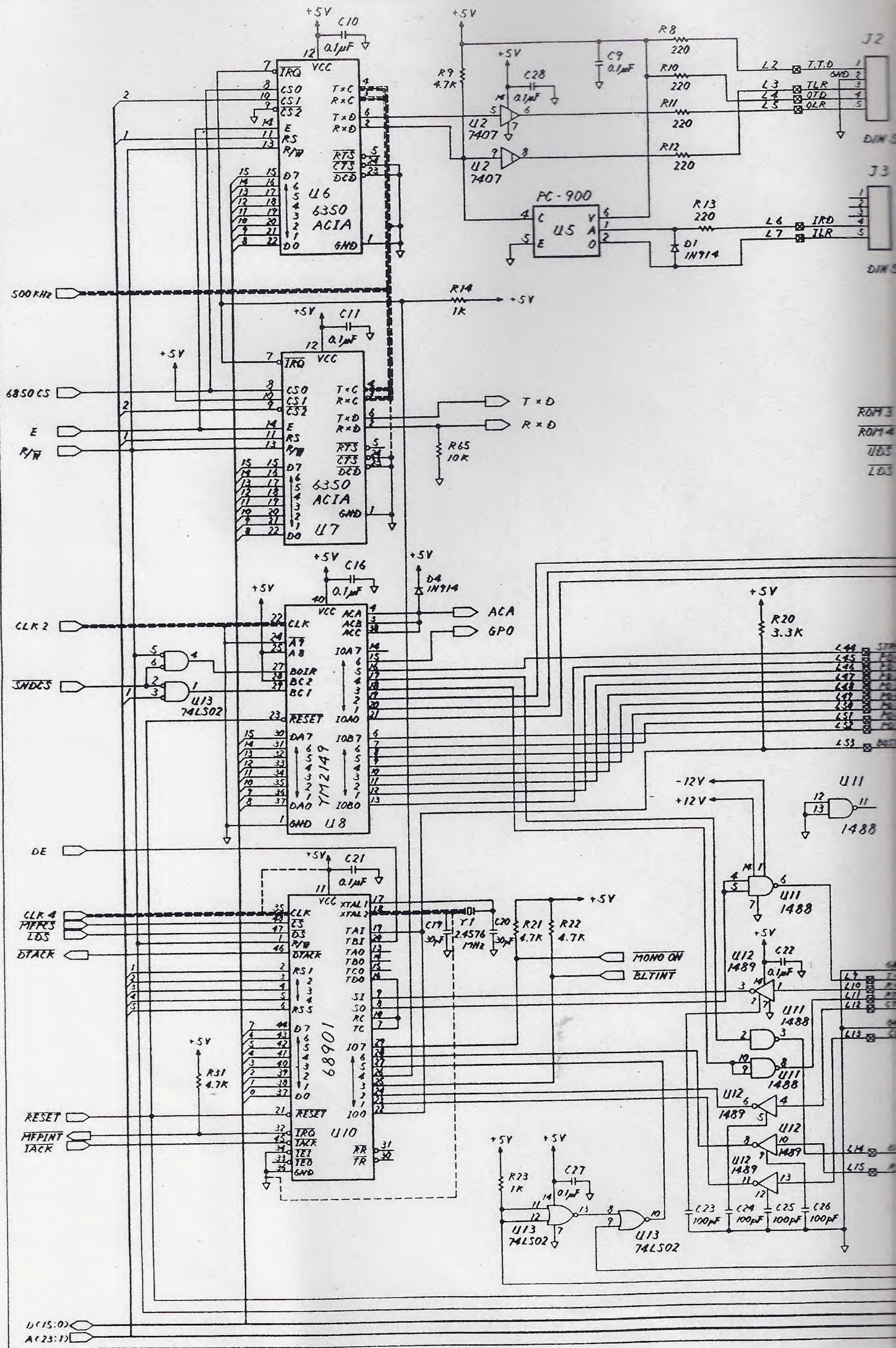


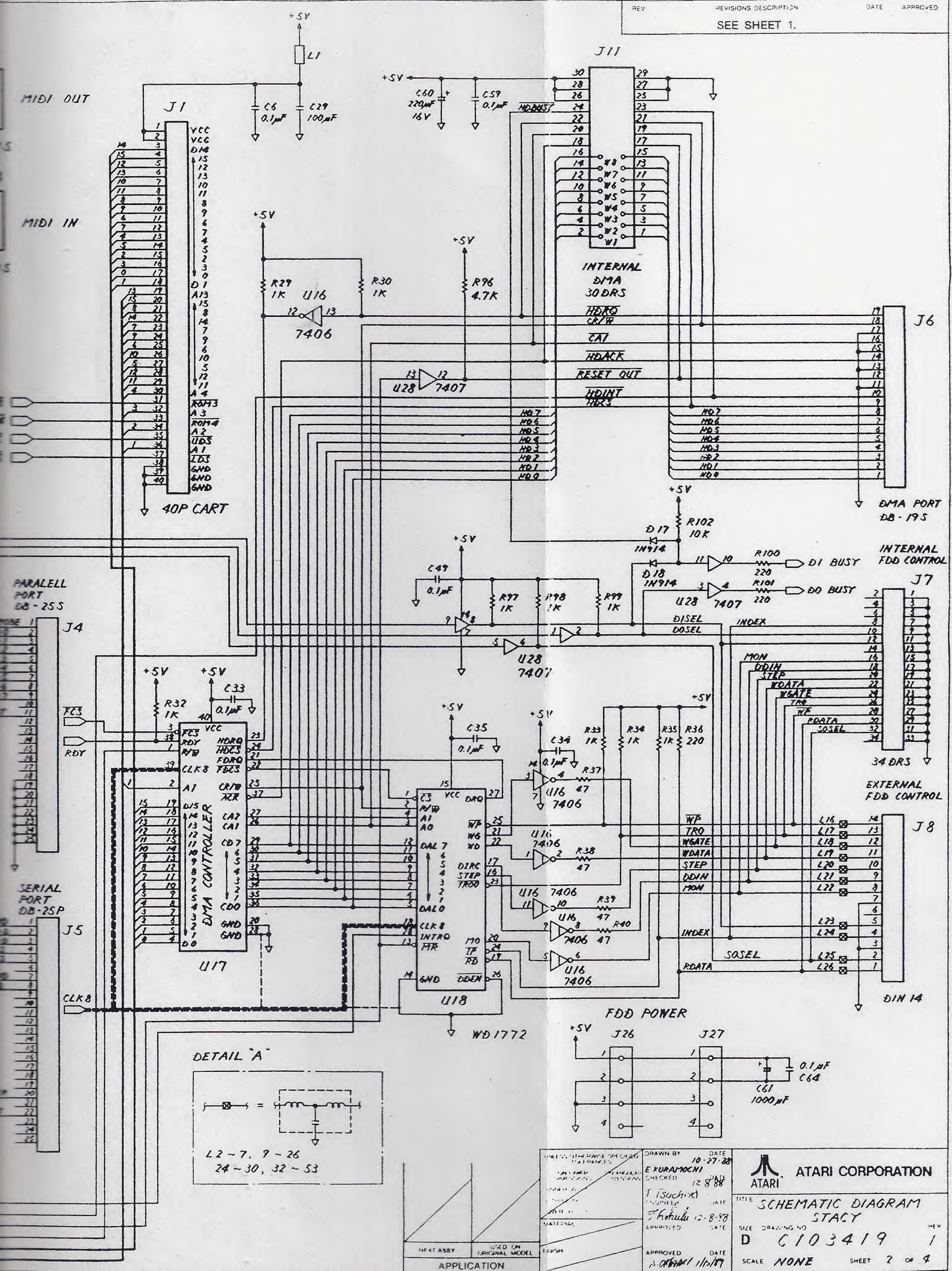
NOTES—UNLESS OTHERWISE SPECIFIED:

UNLESS OTHERWISE SPECIFIED TOLERANCES ON LINEAR DIMENSIONS UNDER 30 ± 30 THRU 300 ± OVER 300 ± MATERIAL FINISH		DRAWN BY <i>L. Luba</i> 6-2-89		DATE 6-2-89	
		CHECKED <i>T. Tschirya</i>		DATE 6-2-89	
NEXT ASSY USED ON ORIGINAL MODEL		ENGINEER <i>A. Shinagawa</i> 6-2-89		TITLE ASSY SUB PCB LST-4	
APPLICATION		APPROVED <i>[Signature]</i> 6/3/89		DATE 6/3/89	
		APPROVED <i>S. OKADA</i> 6/3/89		DATE 6/3/89	
		SIZE B		DRAWING NO. CA 200.528-xxx	
		SCALE NONE		SHEET 6 OF 6	

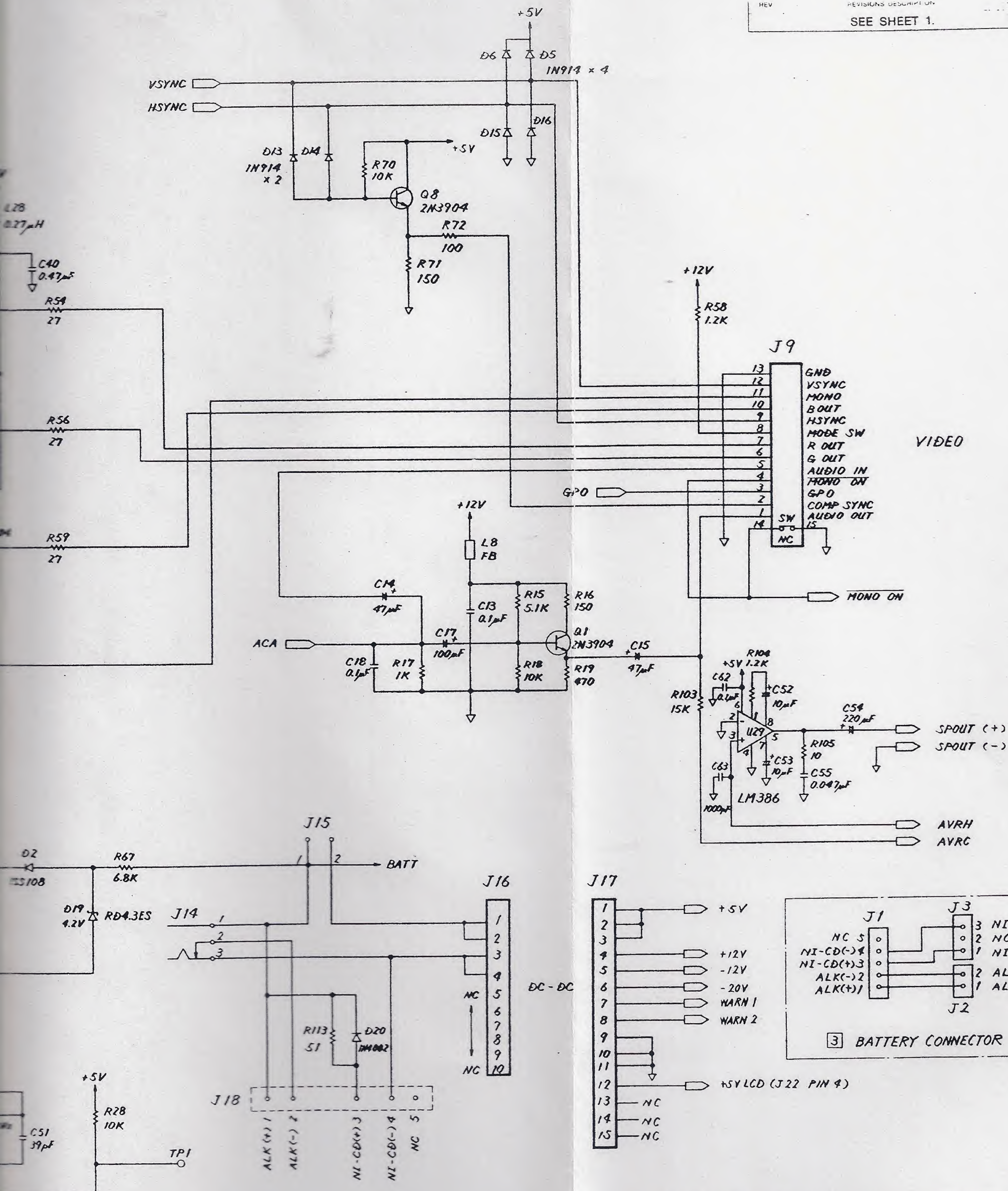
ATARI CORPORATION

THRU HOLE & SYSTEM DIMENSIONS: INCH



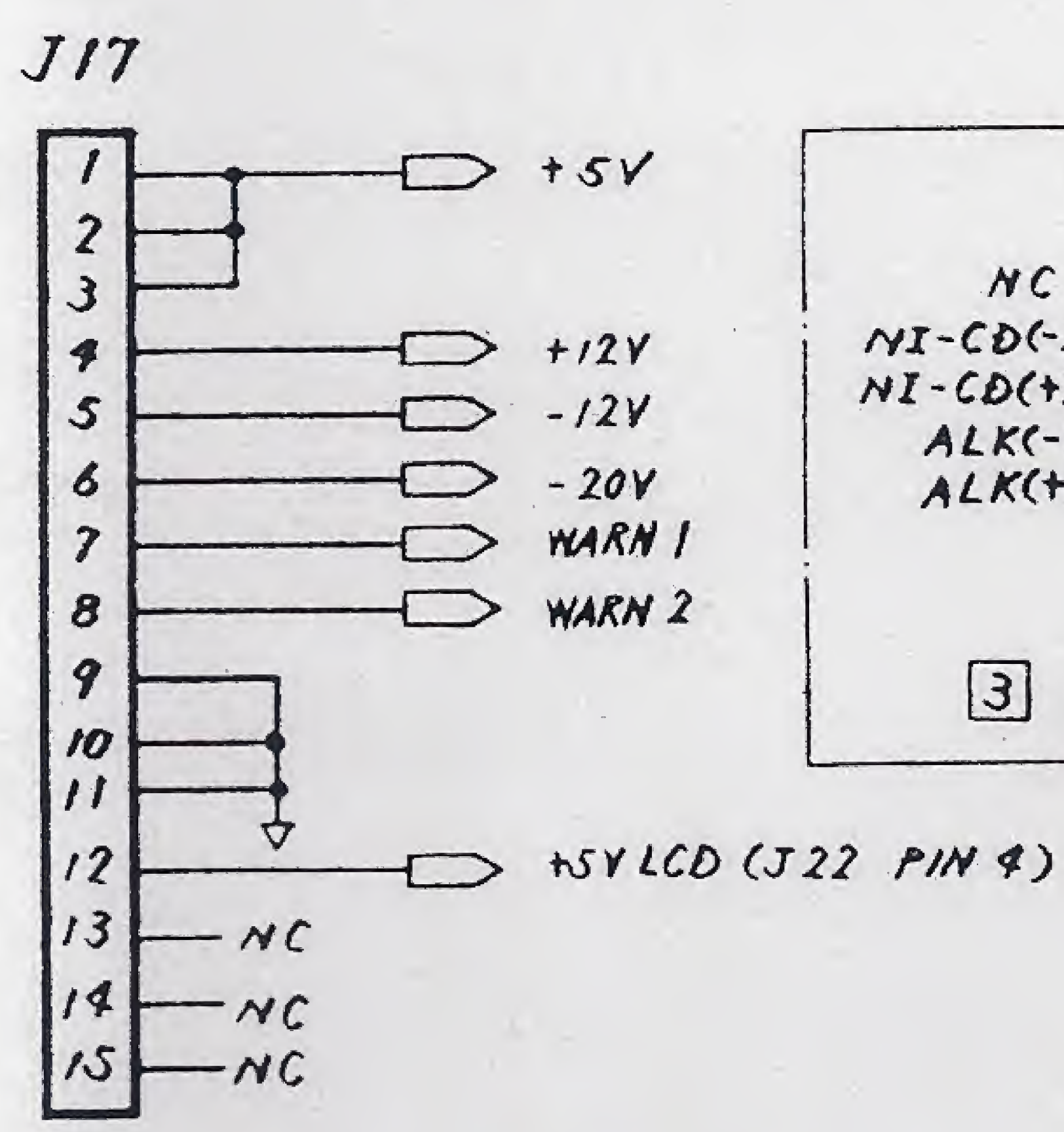
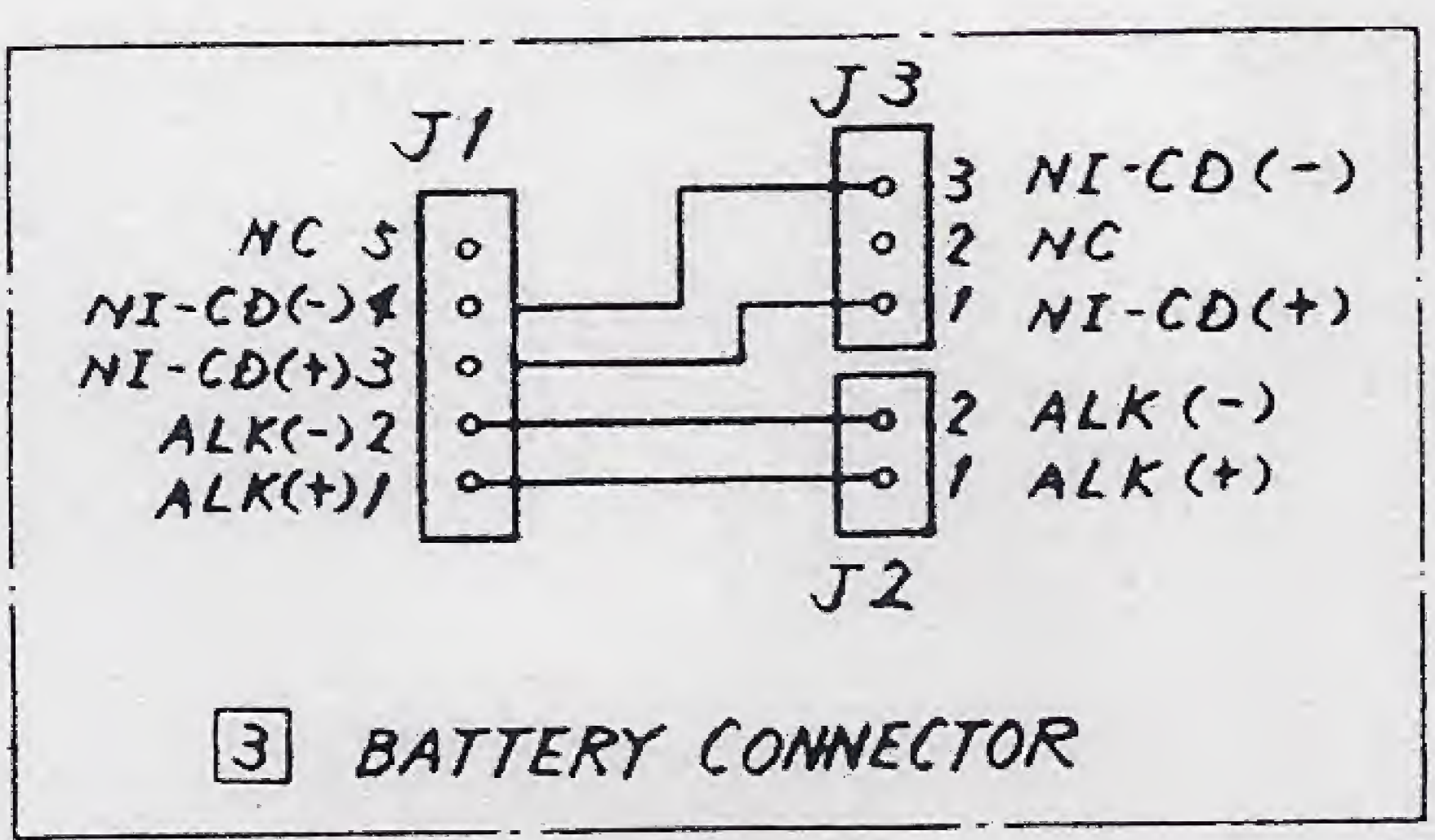


DATE: 10-27-88 DRAWN BY: E. KURAMOTO CHECKED: 12-8-88 DATE: 12-8-88 APPROVED: 12-8-88 DATE: 12-8-88	ATARI CORPORATION SCHEMATIC DIAGRAM STACY SIZE: DRAWING NO. D C103419 SCALE: NONE SHEET 2 OF 4
--	---

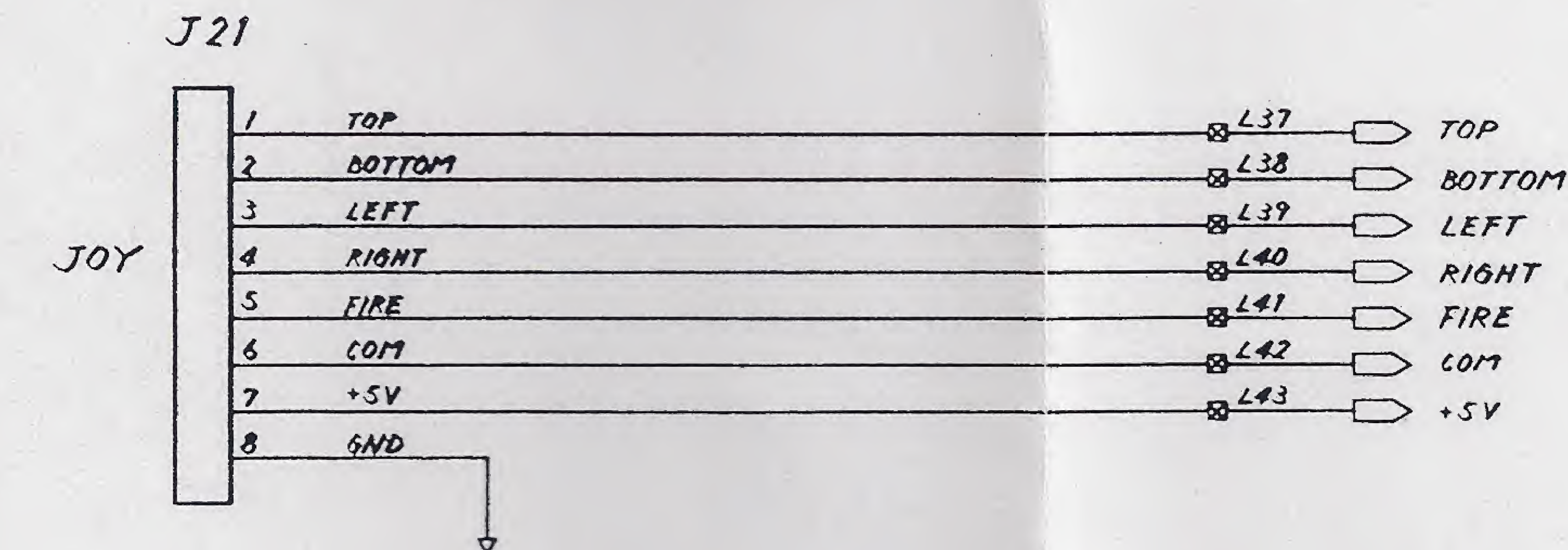
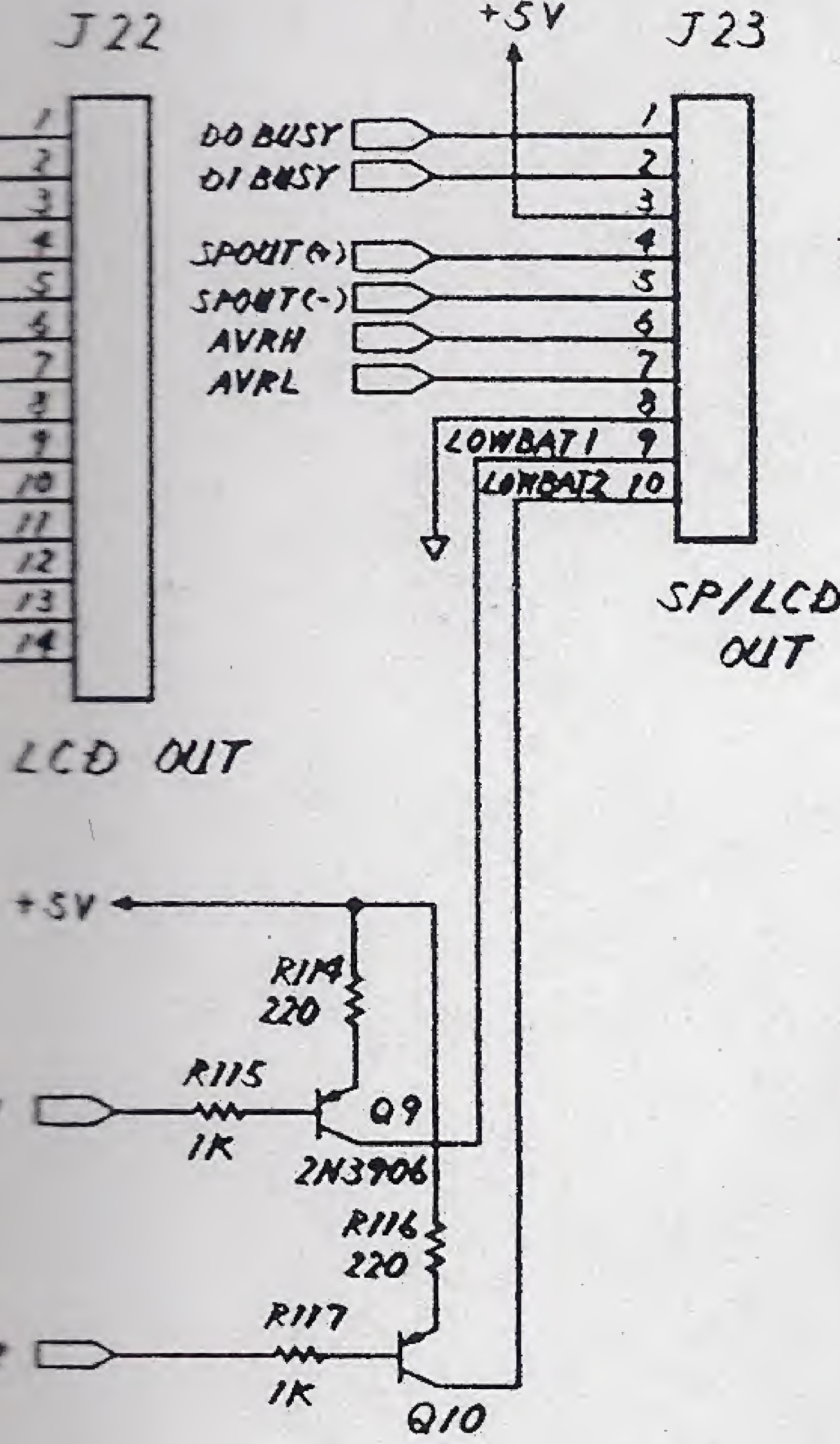




VIDEO

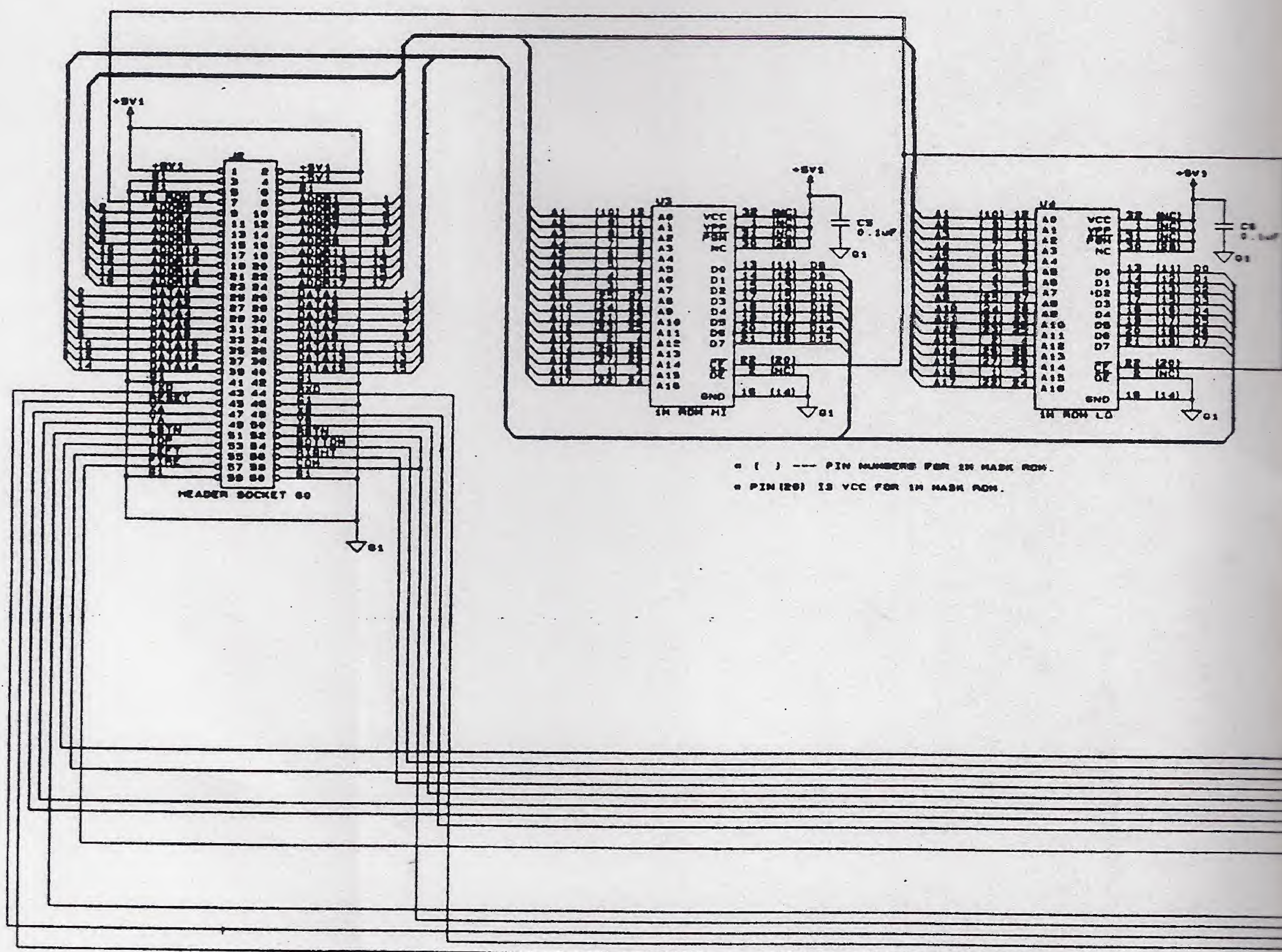
AVRH
AVRC



UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS IN INCHES DIMENSIONS IN PARENTHESES ARE METRIC EQUIVALENTS DIMENSIONS UNDER 10 INCHES TO THREE DECIMAL PLACES DIMENSIONS OVER 10 INCHES TO TWO DECIMAL PLACES DATE THAT		DRAWN BY <i>K. Hanks</i> 10-26-88 CHECKED 12-8-88 ENGINEER DATE 12-8-88 APPROVED DATE 12-8-88	ATARI CORPORATION TITLE SCHEMATIC DIAGRAM STACY SIZE DRAWING NO D C103419 SCALE NONE SHEET 3 OF 9
NEXT ASSY USED ON ORIGINAL MODEL FINISH		APPLICATION	

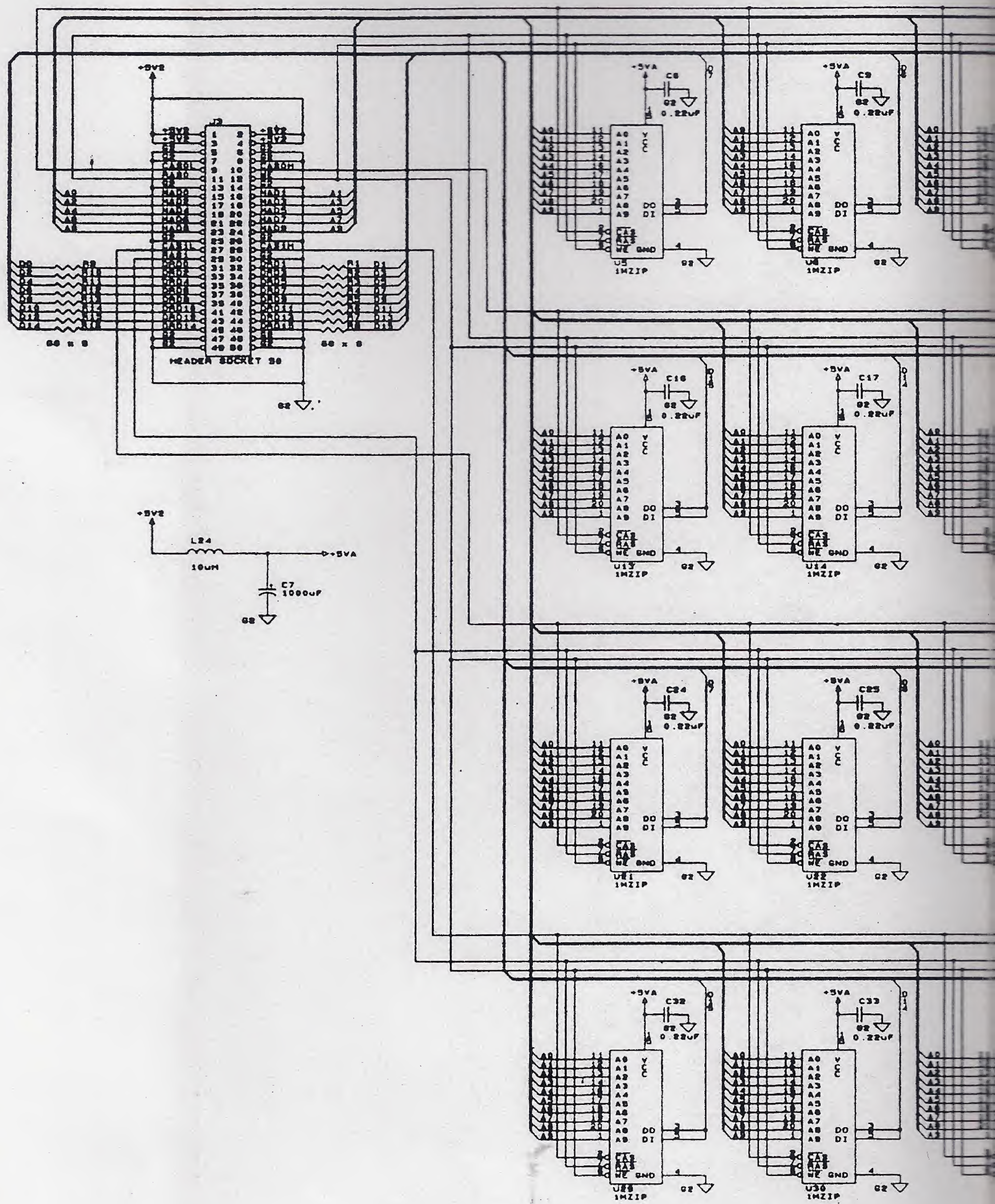


	STRESS (OTHERWISE SPECIFIED) DIMENSIONS FINISH	DRAWN BY E. KURAMOTO CHECKED 12-8-88	DATE 10-31-88 DATE 12-8-88	 ATARI CORPORATION	
	USED ON ORIGINAL MODEL	F. Tsuchida ENGINEER APPROVED 12-10-88	DATE 12-10-88 DATE		TITLE SCHEMATIC DIAGRAM STACY
NEXT ASSY APPLICATION	FINISH	APPROVED A. DUMAR	DATE 1/1/89	SIZE (DRAWING NO) D C103419	REV 1
				SCALE NONE	SHEET 4 OF 4



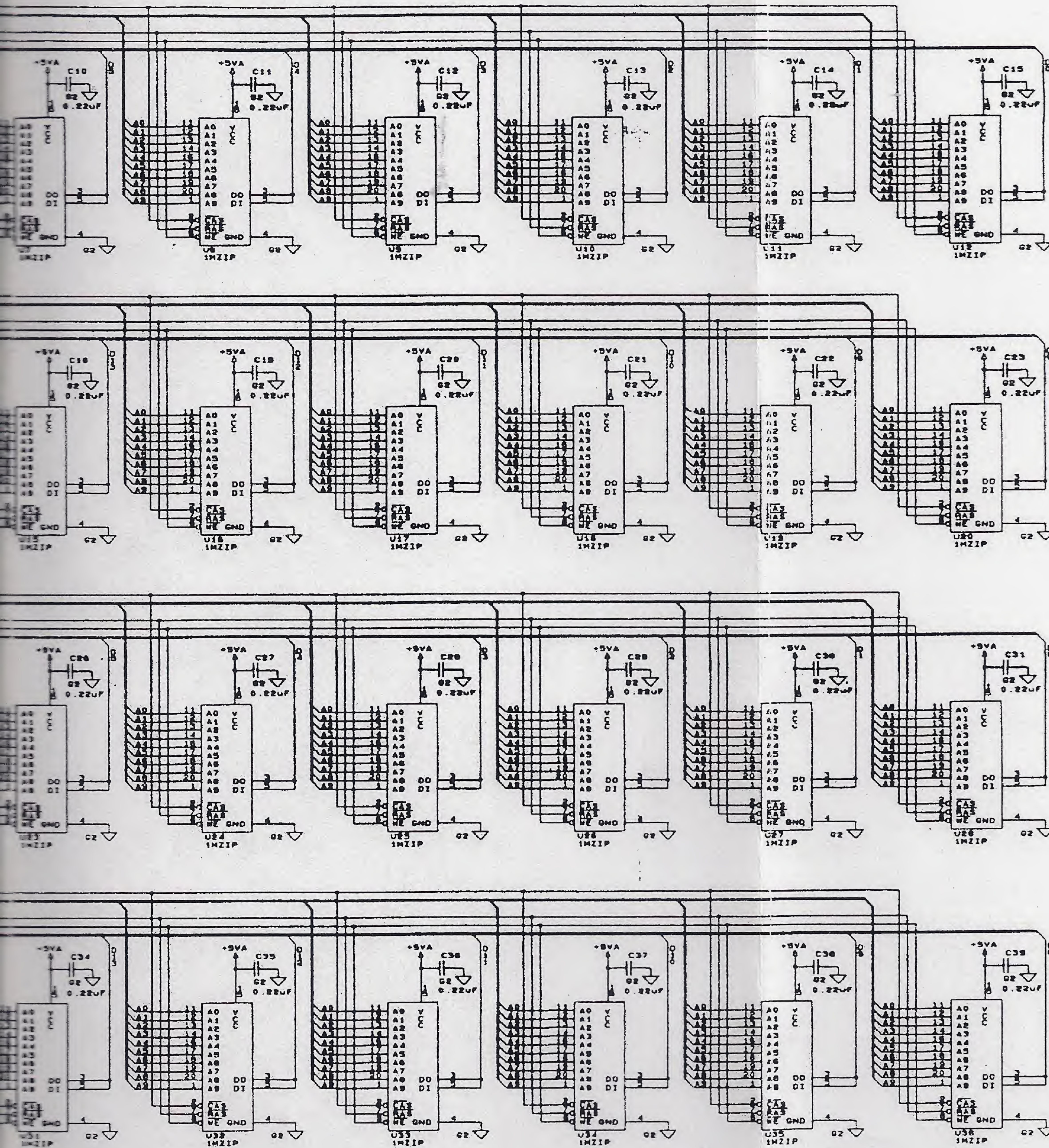
1. RESISTORS ARE MEASURED IN OHMS.

NOTES—UNLESS OTHERWISE SPECIFIED



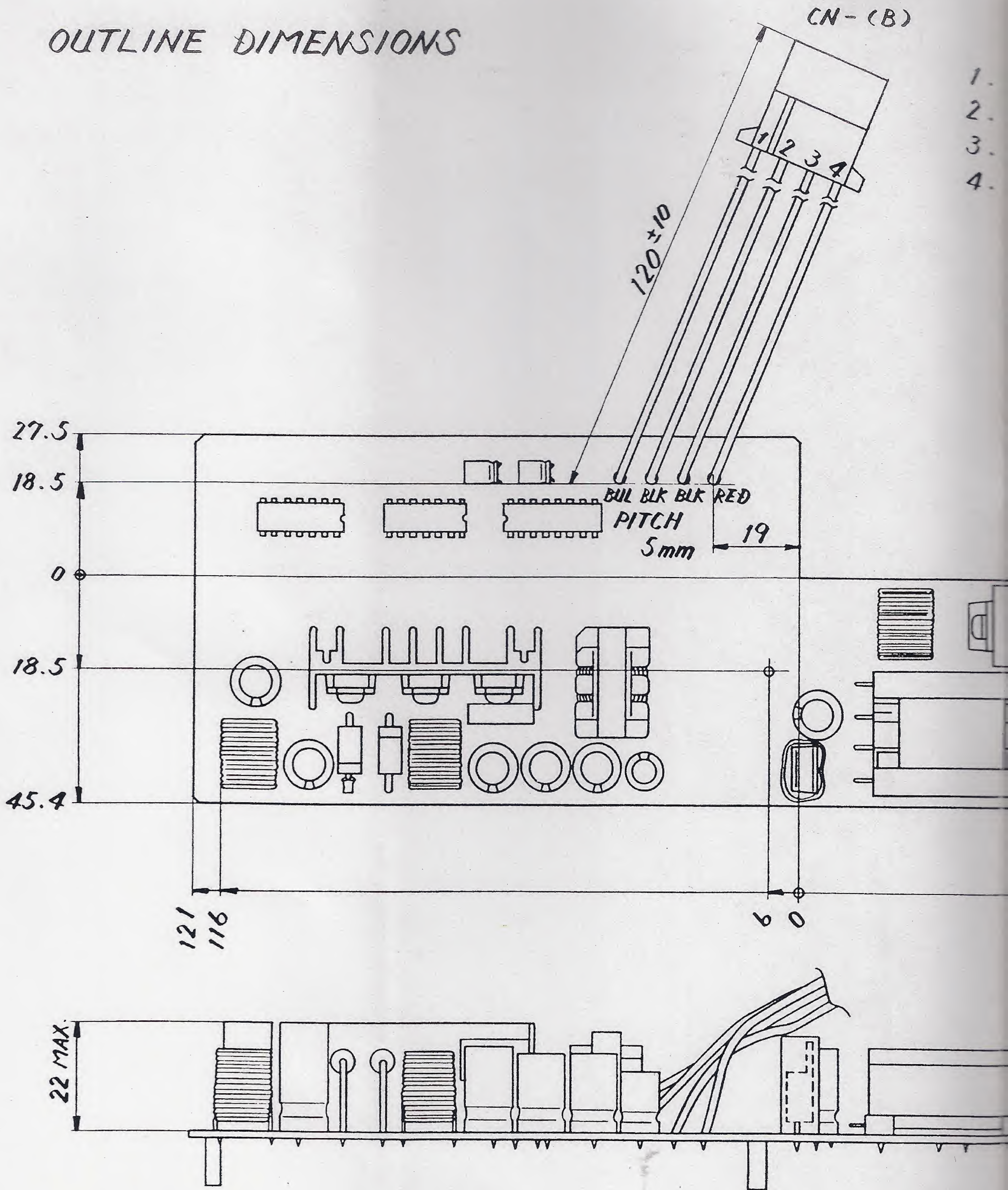
NOTES—UNLESS OTHERWISE SPECIFIED

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS DIMENSIONS IN PARENTHESES ARE IN INCHES		DRAWN BY 2. Kanda 5-24-89 CHECKED 5-24-89 T. Tsuchida ENGINEER O. Shingawa 5-31-89 APPROVED 6/1/89	DATE DATE DATE DATE DATE
APPLICATION SUB PCB LST-4		ATARI CORPORATION ATARI SCHEMATIC DIAGRAM SUB PCB LST-4 DRAWING NO. C 103639 B REV D NONE SHEET 2 OF 2	

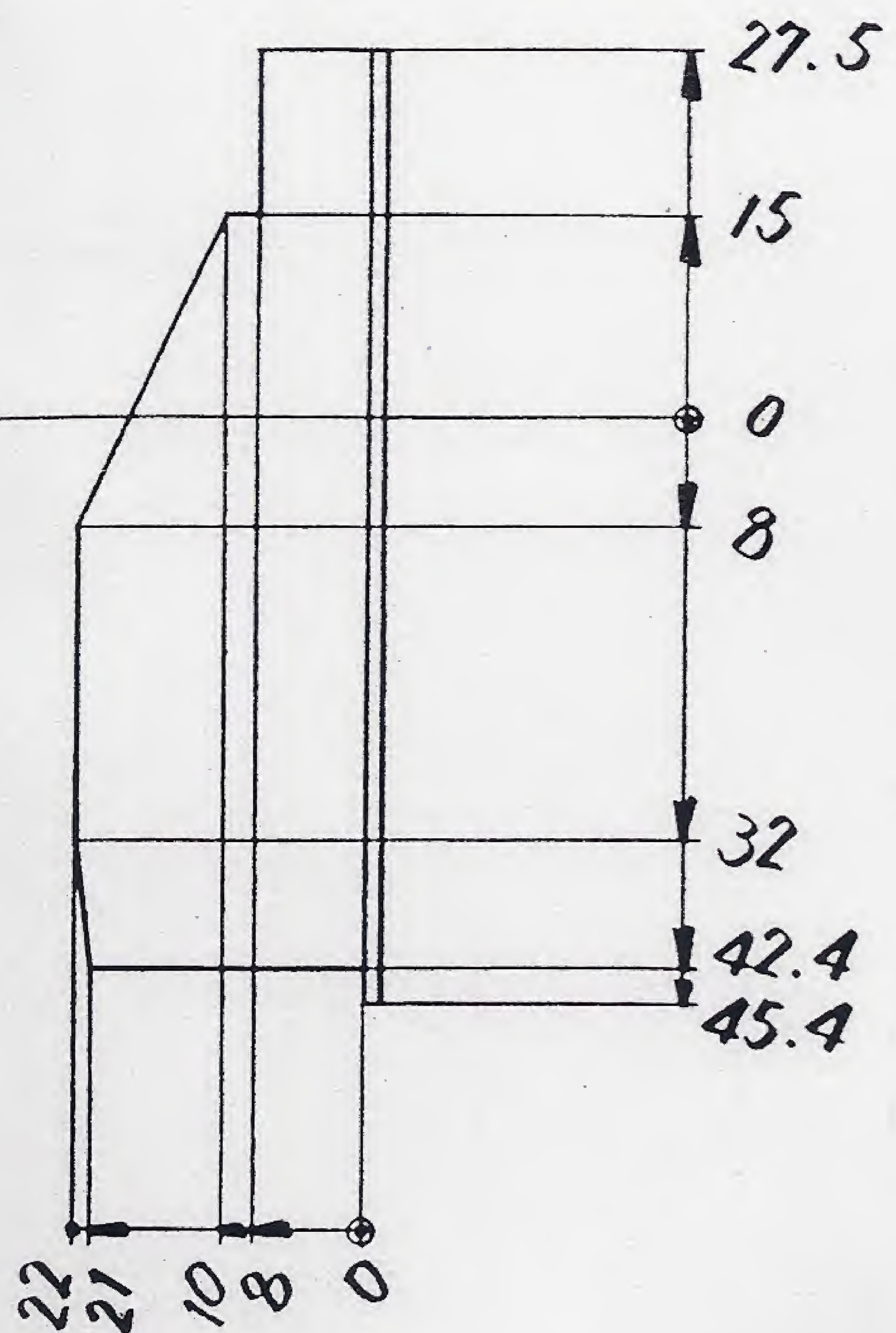
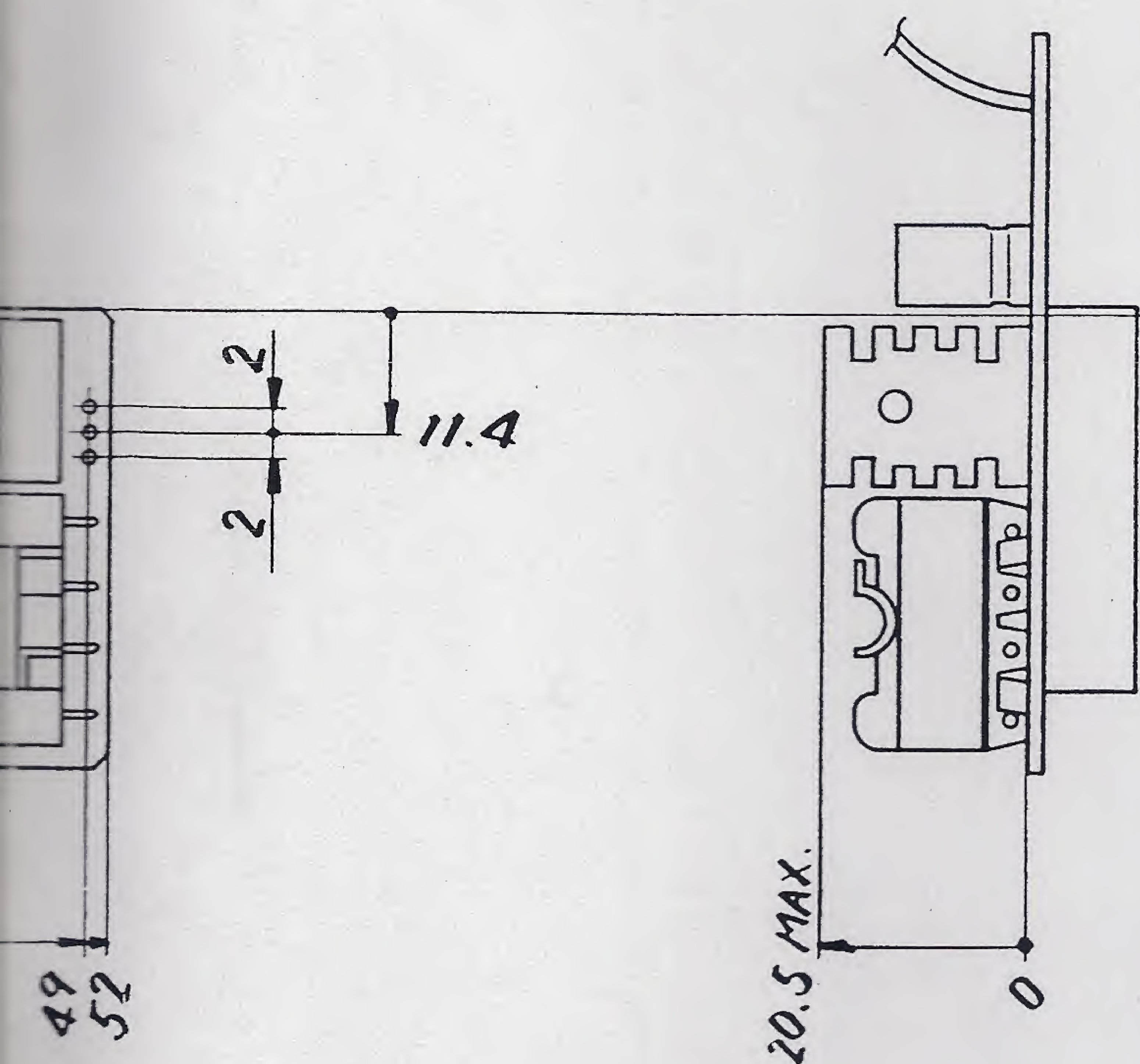
OUTLINE DIMENSIONS




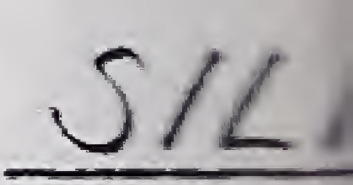
NOTES—UNLESS OTHERWISE SPECIFIED:

NEXT ASSY	ORIG
APPLICATION	

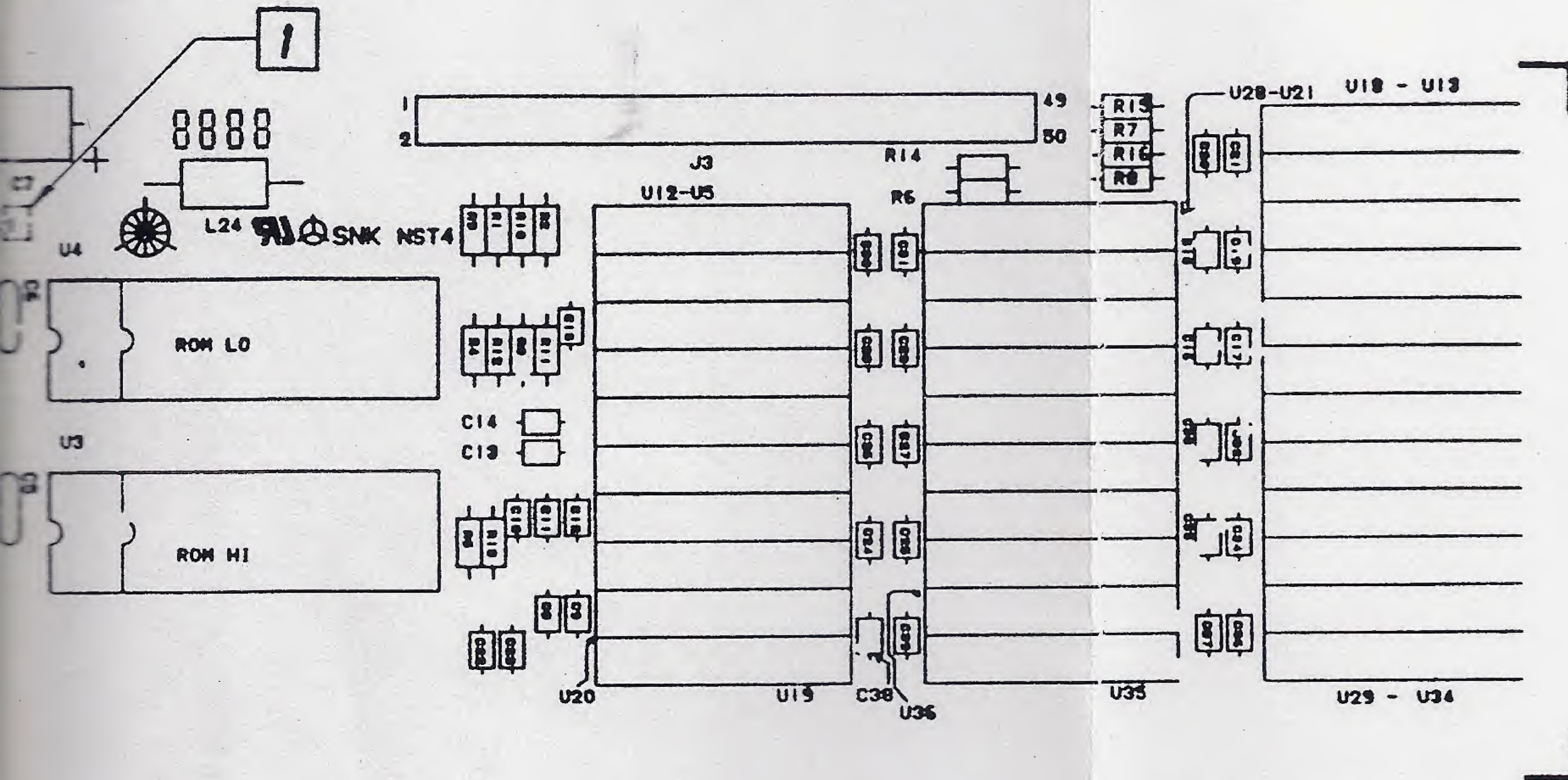
BLU 12V
 BLK GND
 BLK GND
 RED 5V




UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY <i>H. Kamba</i> 2-26-90		DATE		 ATARI CORPORATION					
ON LINIER DIMENSIONS		ON ANGULAR DIMENSIONS		CHECKED <i>VN</i> 2/27/90				DATE			
UNDER 30		± 0.1		± 0.5°		ENGINEER		DATE			
30 THRU 300		± 0.2				2-28-90		A. Shinagawa			
OVER 300		± 0.4				APPROVED		DATE			
MATERIAL				APPROVED		DATE		TITLE			
FINISH				APPROVED		DATE		DC - DC CONVERTER			
USED ON ORIGINAL MODEL				N. OKUBA 2/28/90		SCALE		NONE			
ON						SIZE		DRAWING NO.			
						B		C101780-xxx			
								REV			
								B			
						SHEET		7 OF 7			

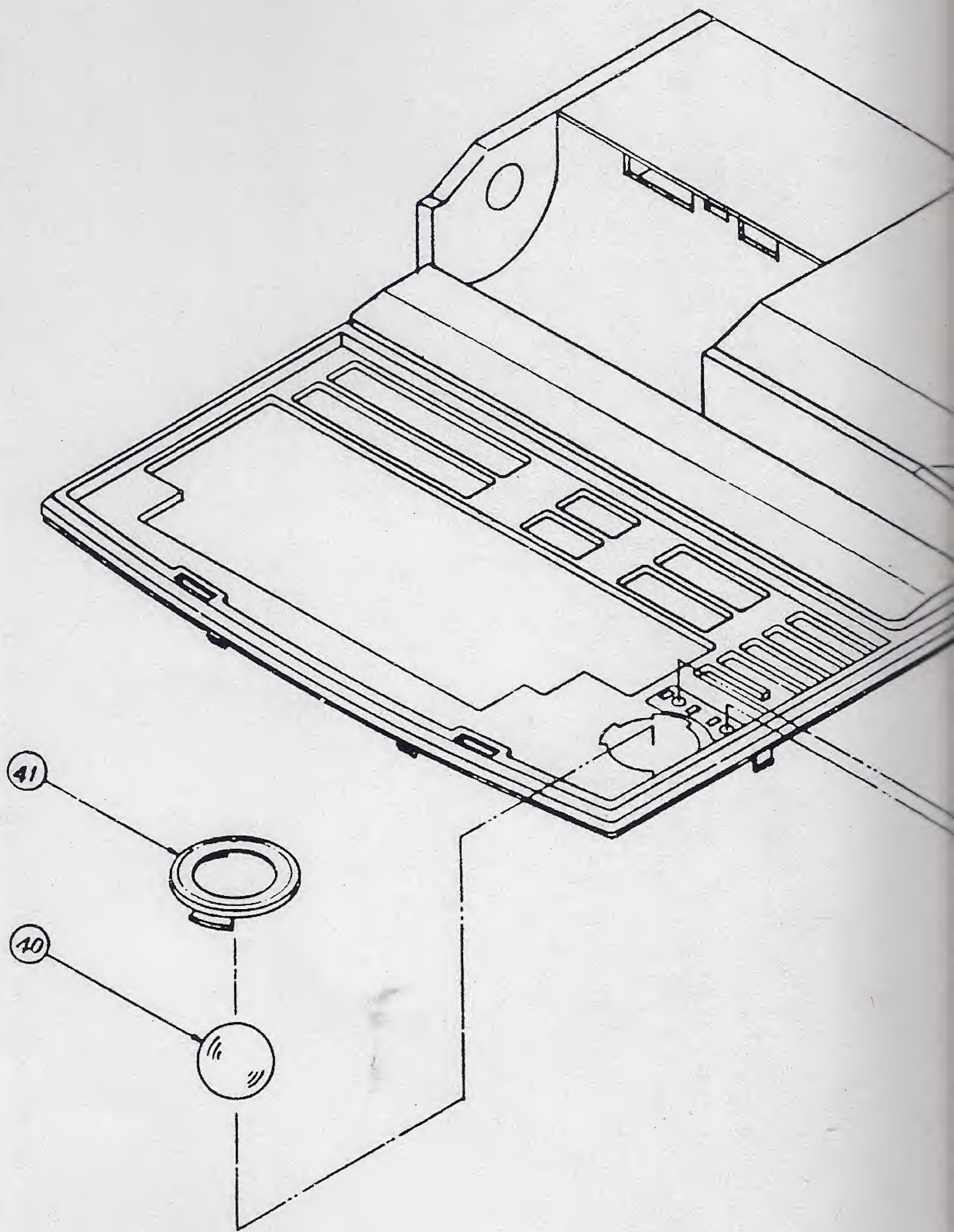


REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



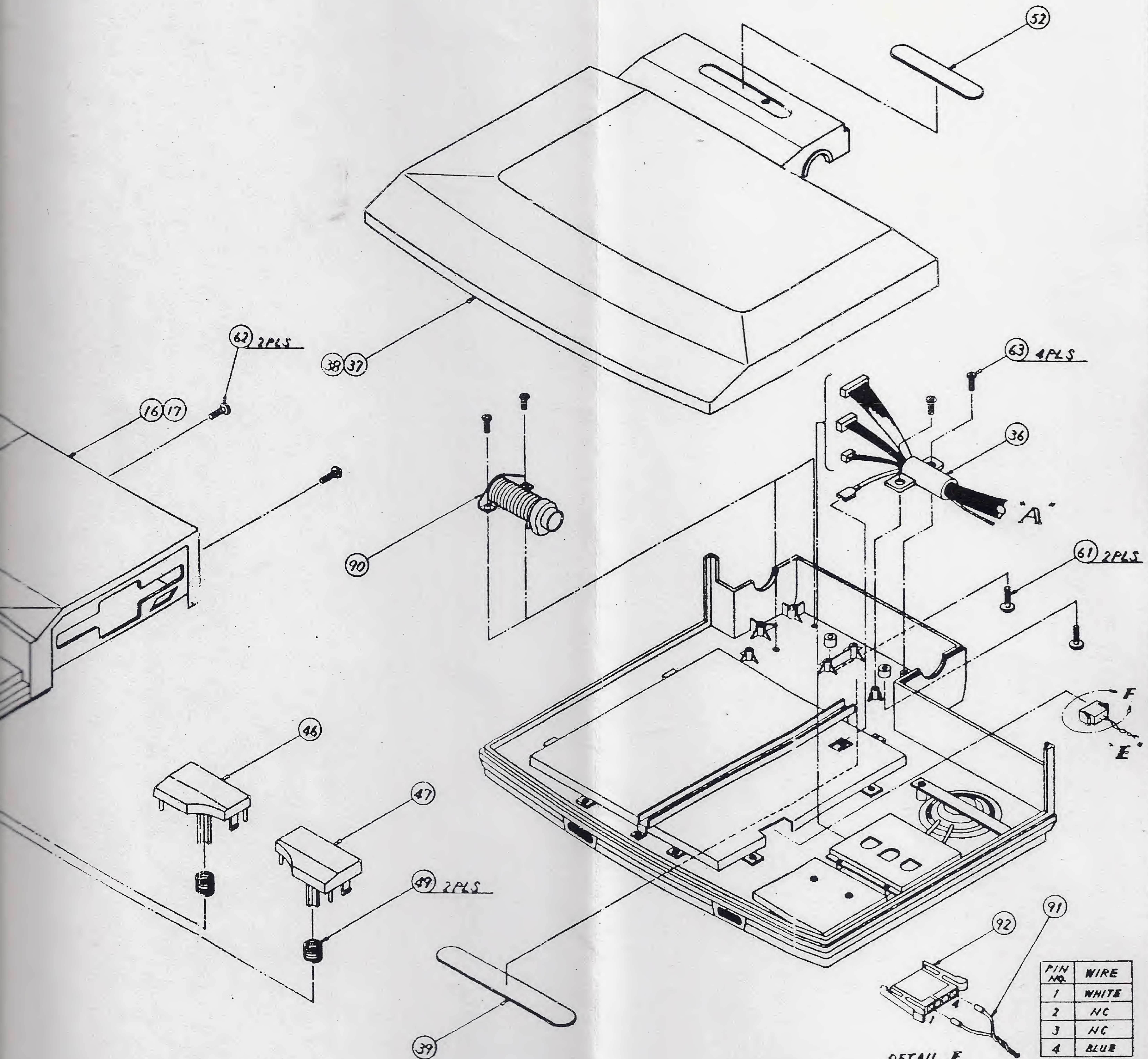
K PRINT

UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY <i>H. Lanza</i> 7-27-89		DATE 7-27-89		 ATARI CORPORATION	
ON LINER DIMENSIONS	ON ANGULAR DIMENSIONS	CHECKED <i>T. Tsuchida</i>	DATE 7-27-89	ENGINEER <i>A. Shinagawa</i>	DATE 8-1-89		
UNDER 30	± 0.1	± 0.5°	APPROVED	DATE	SIZE B	DRAWING NO. <i>C103640</i>	REV A
30 THRU 300	± 0.2		APPROVED	DATE			
OVER 300	± 0.4		APPROVED	DATE			
MATERIAL		APPROVED		DATE	SCALE <i>NONE</i>		
FINISH		APPROVED		DATE	SHEET <i>2</i> OF <i>8</i>		
USED ON ORIGINAL MODEL		APPROVED		DATE	THIRD ANGLE SYSTEM DIMENSION mm		



NOTES—UNLESS OTHERWISE SPECIFIED:

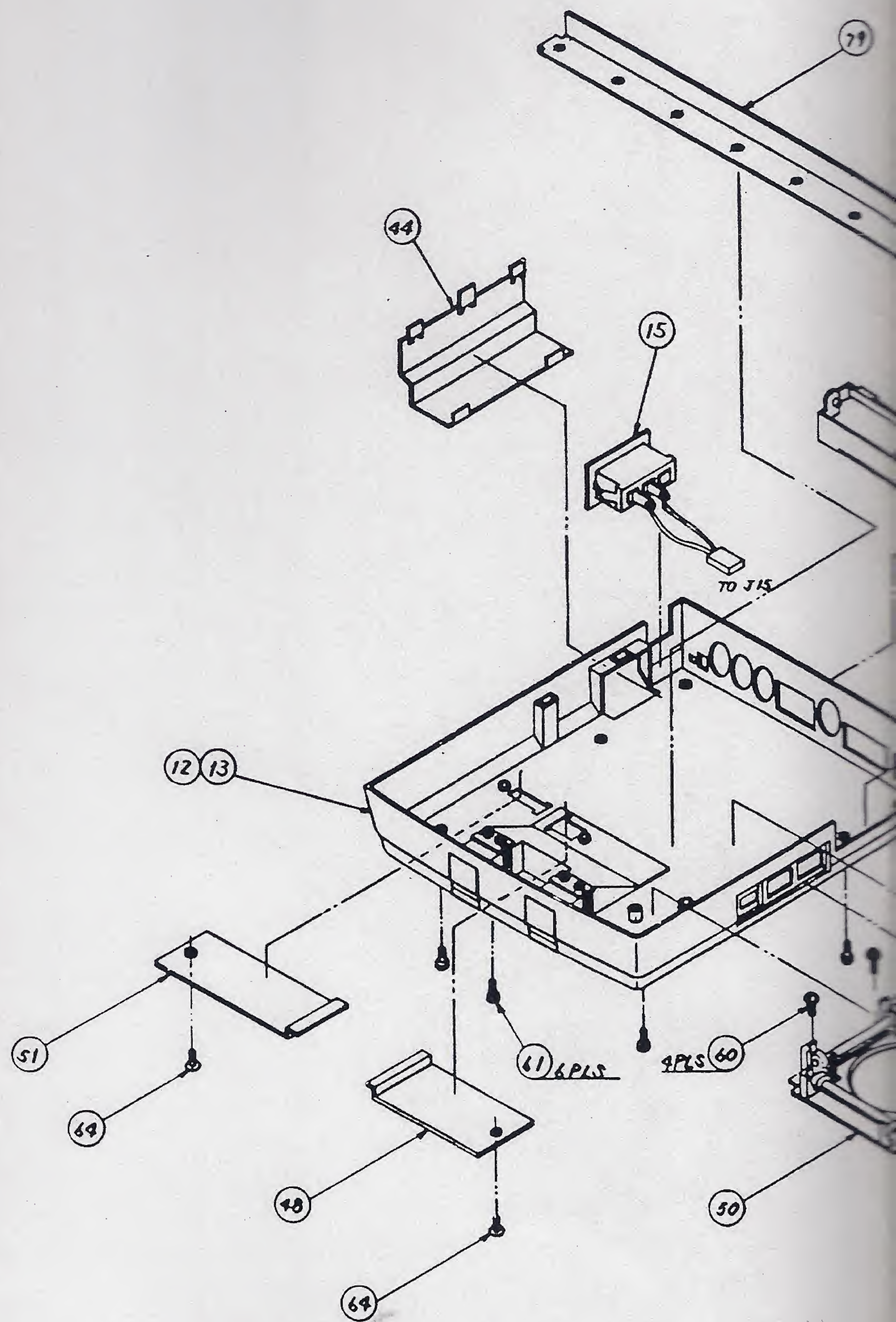
REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



PIN NO.	WIRE
1	WHITE
2	NC
3	NC
4	BLUE

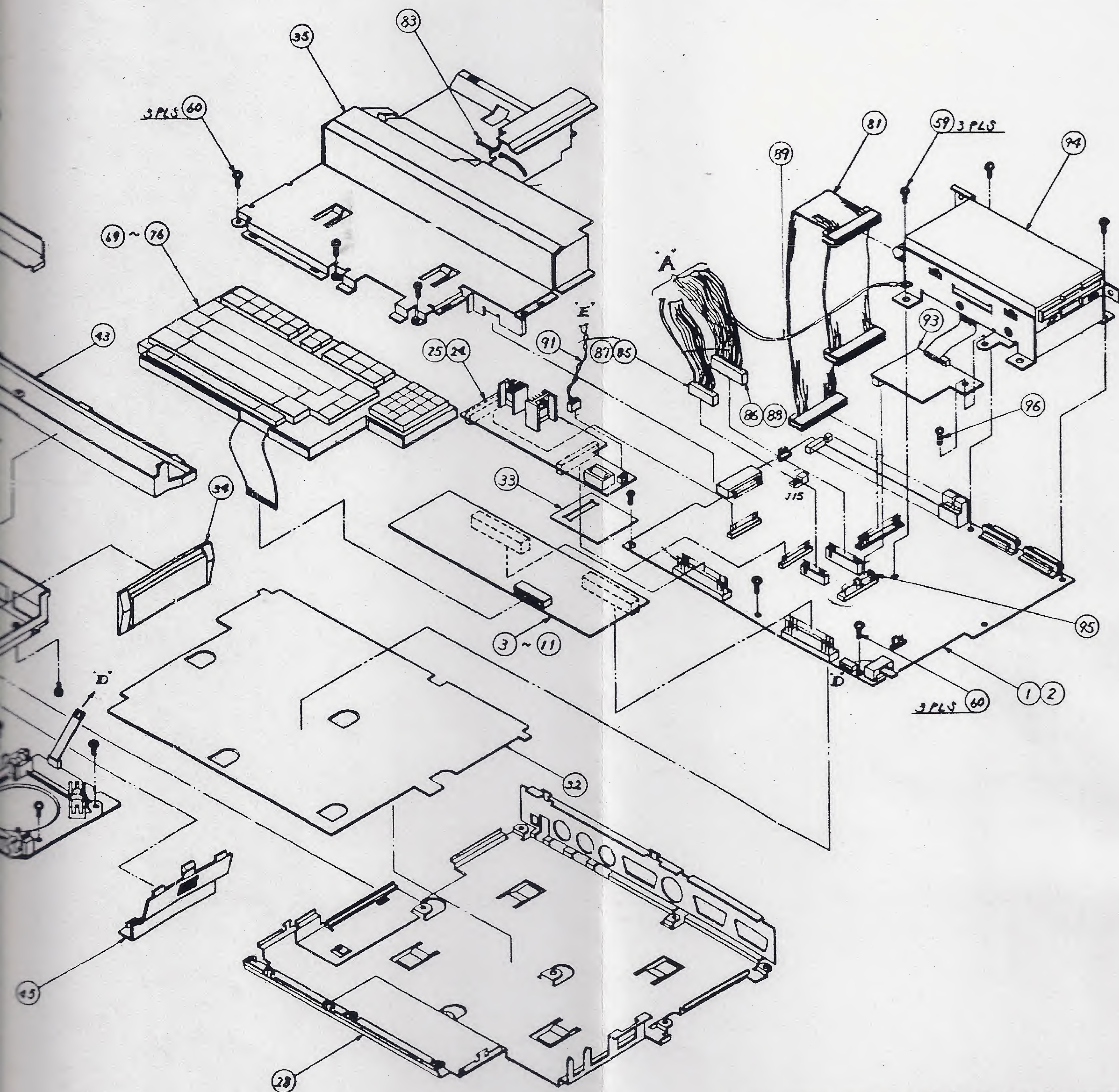
DETAIL E

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN INCHES FRACTIONS ARE IN 16THS OF AN INCH DECIMALS ARE TO 2 PLACES		DRAWN BY S. J. 6-7-80 DATE 6-7-80		ATARI CORPORATION ATARI	
ON LINES CHECKED DATE 6-7-80		ENGINEER T. T. 6-7-80 DATE 6-7-80		TITLE ASSY MAIN LST-4	
MATERIAL FINISH		APPROVED HLS 6/11/80 DATE 6/11/80		SIZE D	
APPLICATION		DRAWING NO. CA200532-xxx		REV D	
		SCALE NONE		SHEET 7 OF 8	

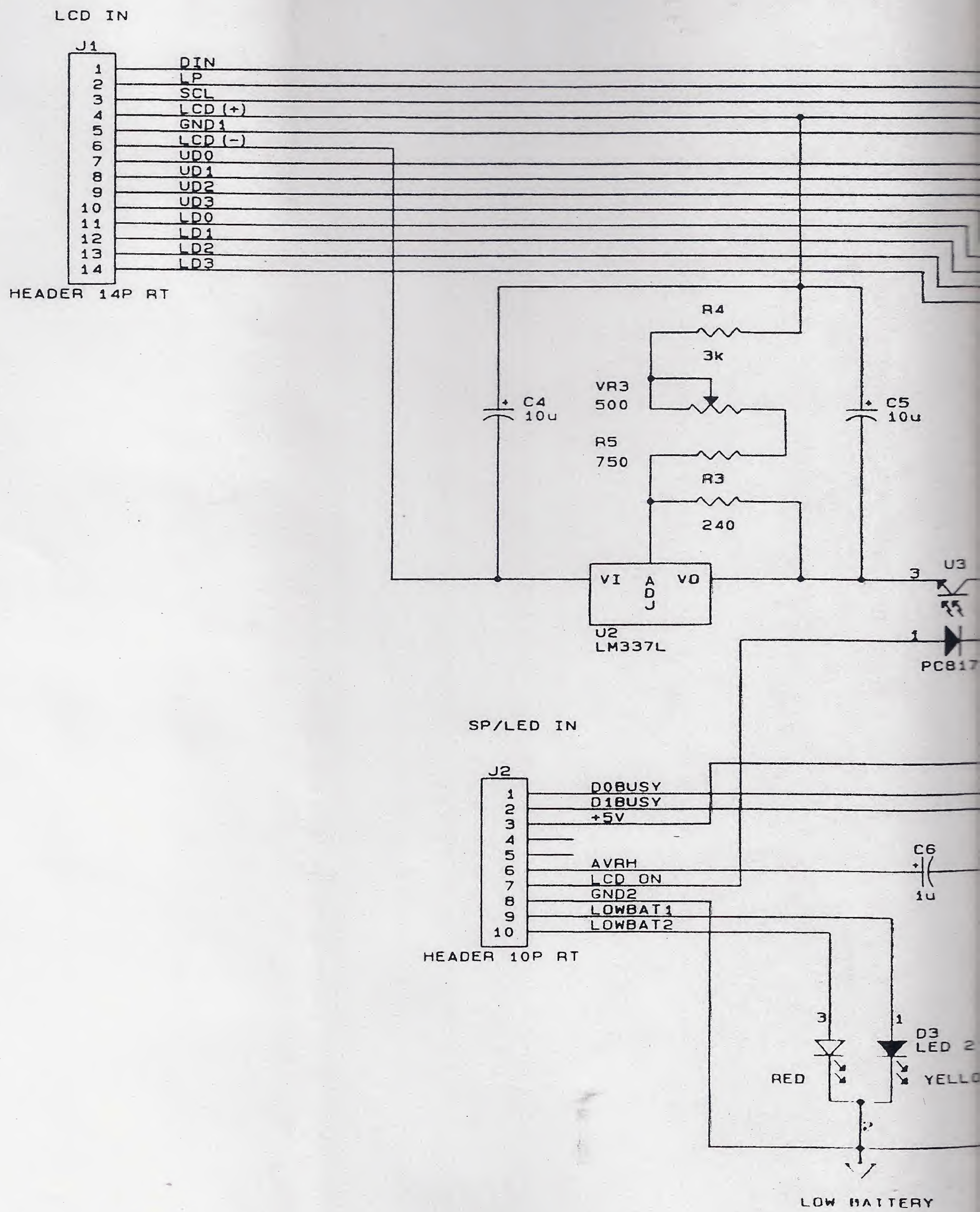


NOTES—UNLESS OTHERWISE SPECIFIED:

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES UNDER 1/8" .01 1/8" TO 1/4" .02 OVER 1/4" .04		CHECKED BY E. F. [Signature] DATE 6-3-79	ATARI CORPORATION ATARI
MATERIAL FINISH		APPROVED BY [Signature] DATE 4/1/79	TITLE ASSY MAIN LST-4 SIZE DRAWING NO. D CA200532-XXX SCALE NONE SHEET 8 OF 8
APPLICATION NEXT ASSY USED ON ORIGINAL MODEL			



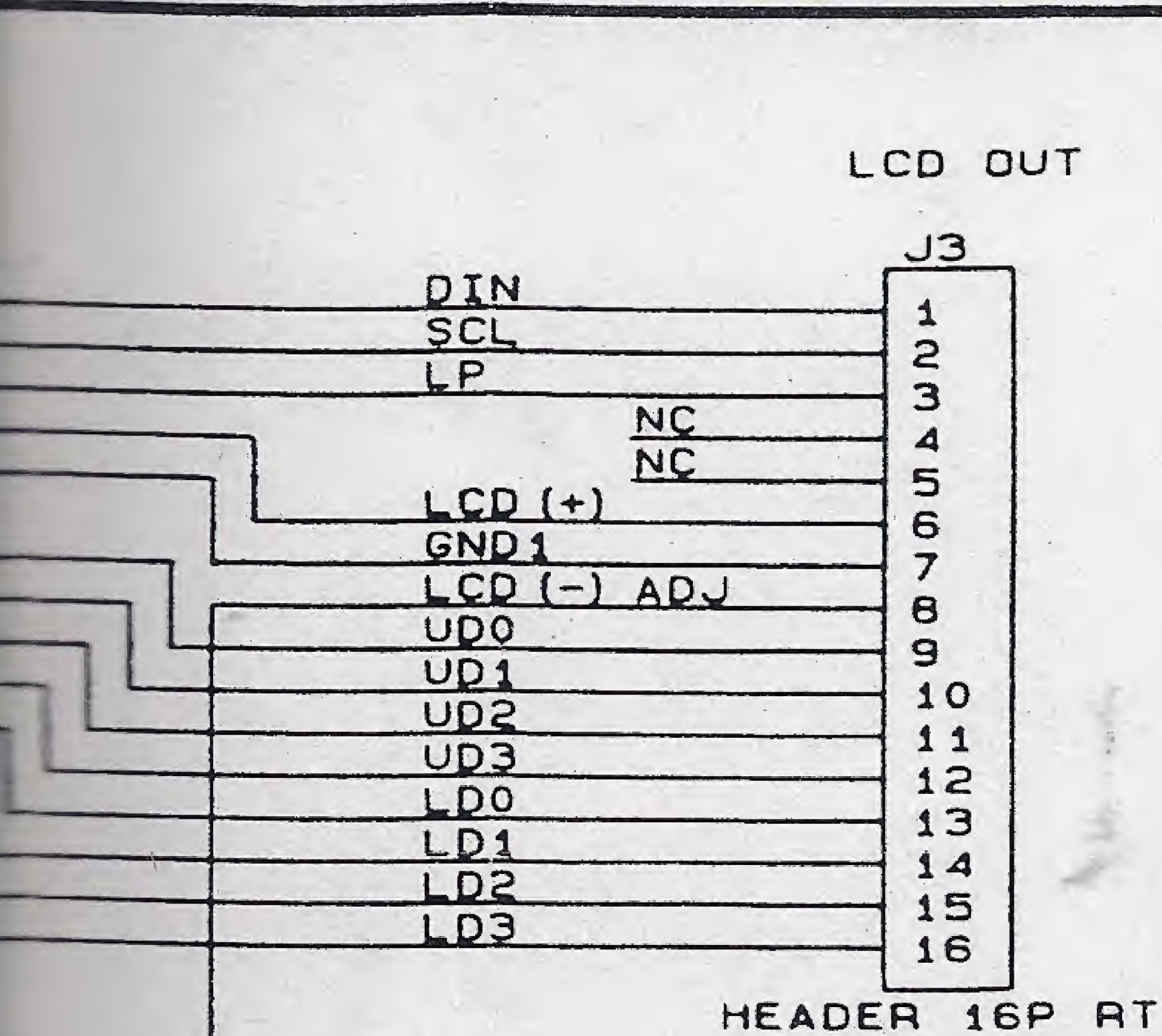
2. CAPACITORS ARE MEASURED IN FARADS.

1. RESISTORS ARE MEASURED IN OHMS.

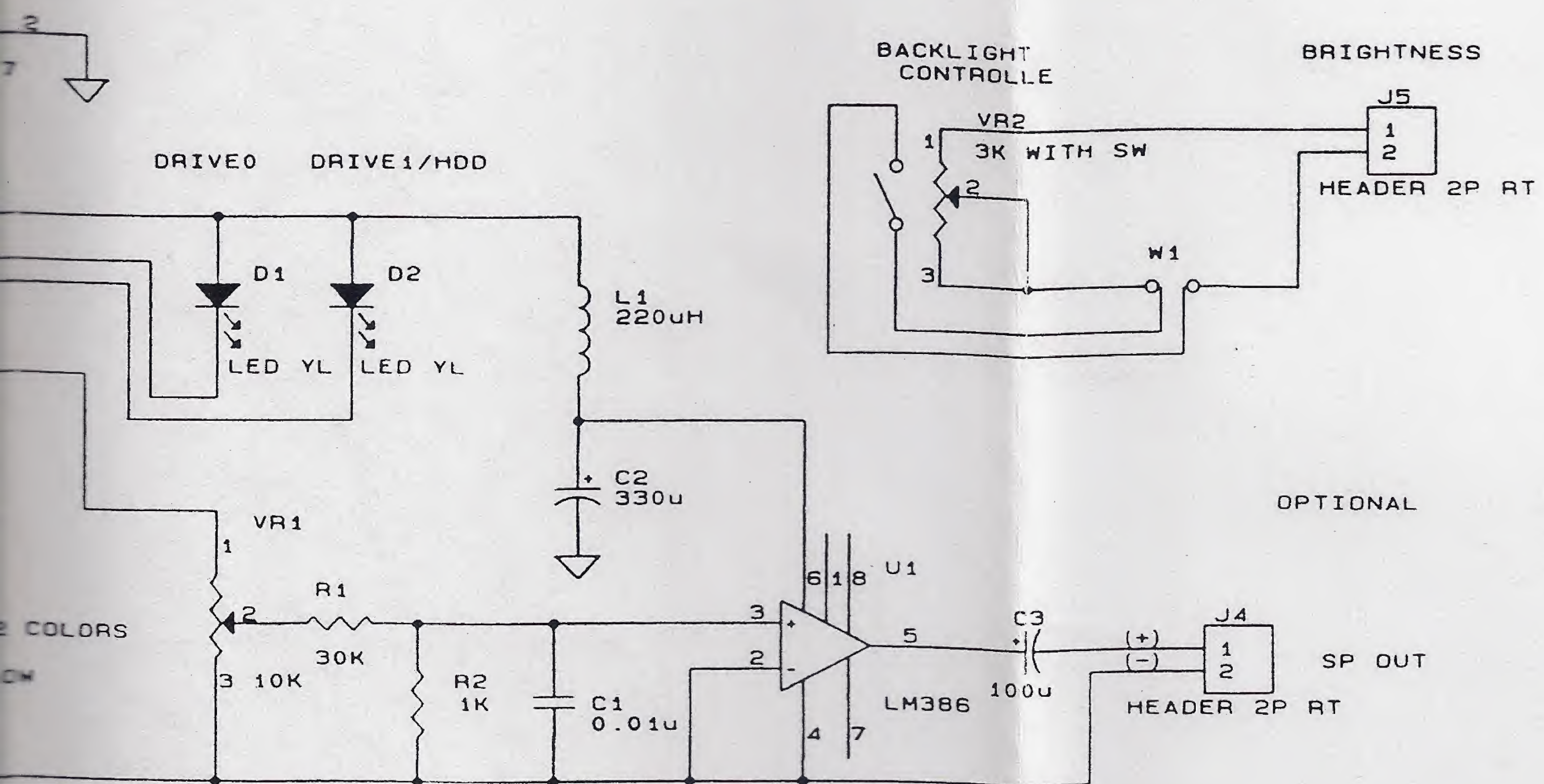
NOTES-UNLESS OTHERWISE SPECIFIED:


CA200

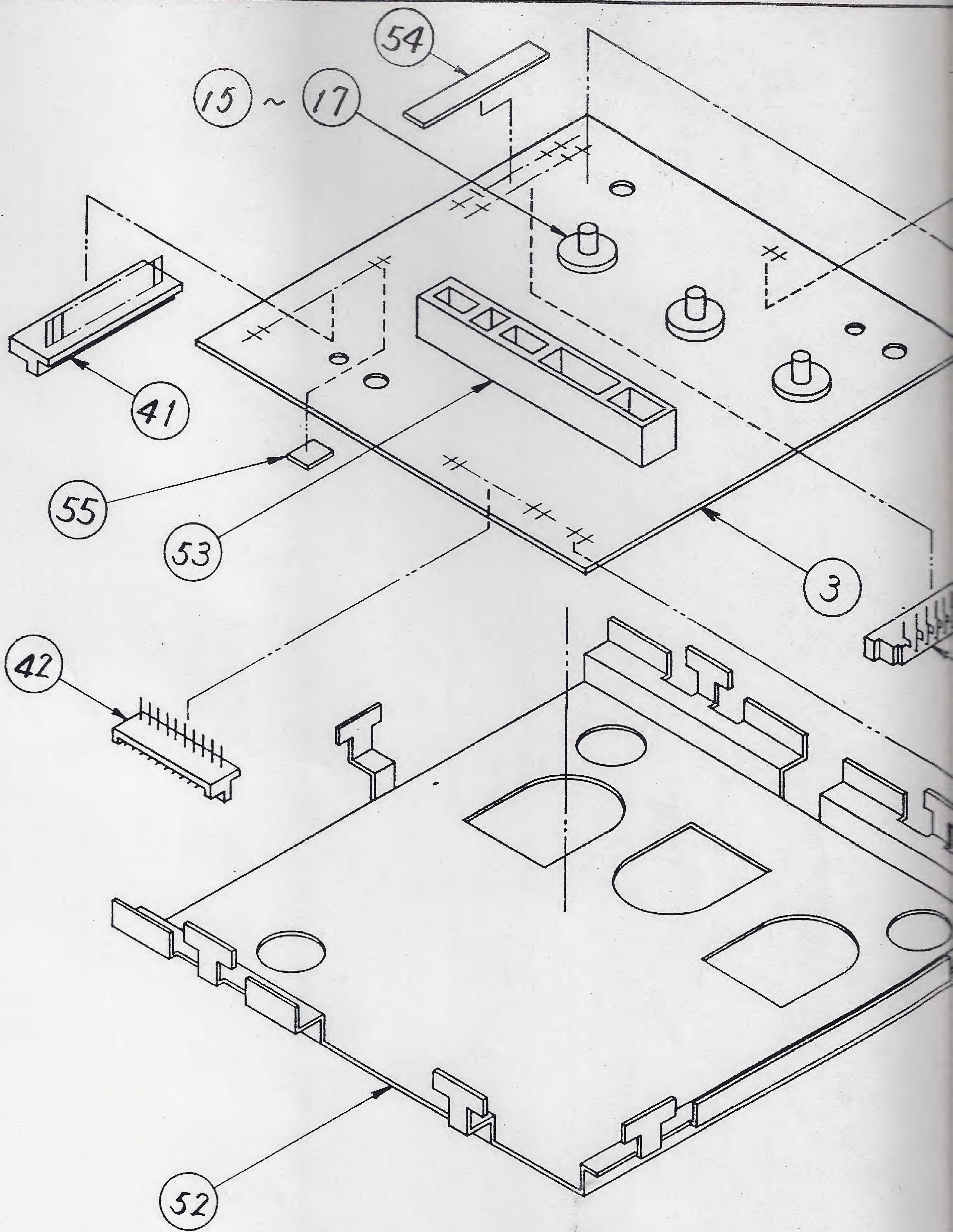
NEXT



REV	REVISIONS DESCRIPTION	DATE	APPROVED
A	PRODUCTION RELEASE	10-26-89	<i>[Signature]</i>



		DRAWN BY	DATE	 ATARI	ATARI CORPORATION		
		A. SHINAGAWA	03/29/89				
		CHECKED	DATE	TITLE	SCHEMATIC DIAGRAM LED PCB		
		I. ISUCHIYA	04/06/89				
		ENGINEER	DATE	SIZE	DRAWING NO.		
		A. SHINAGAWA	03/29/89				
2478-XXX	LST	APPROVED	DATE	B	C103509		
	USED ON						
T ASSY	ORIGINAL MODEL	APPROVED	DATE	REV A			
APPLICATION		S. OKAZAKI	04/10/89				
				SHEET 1 OF 1			

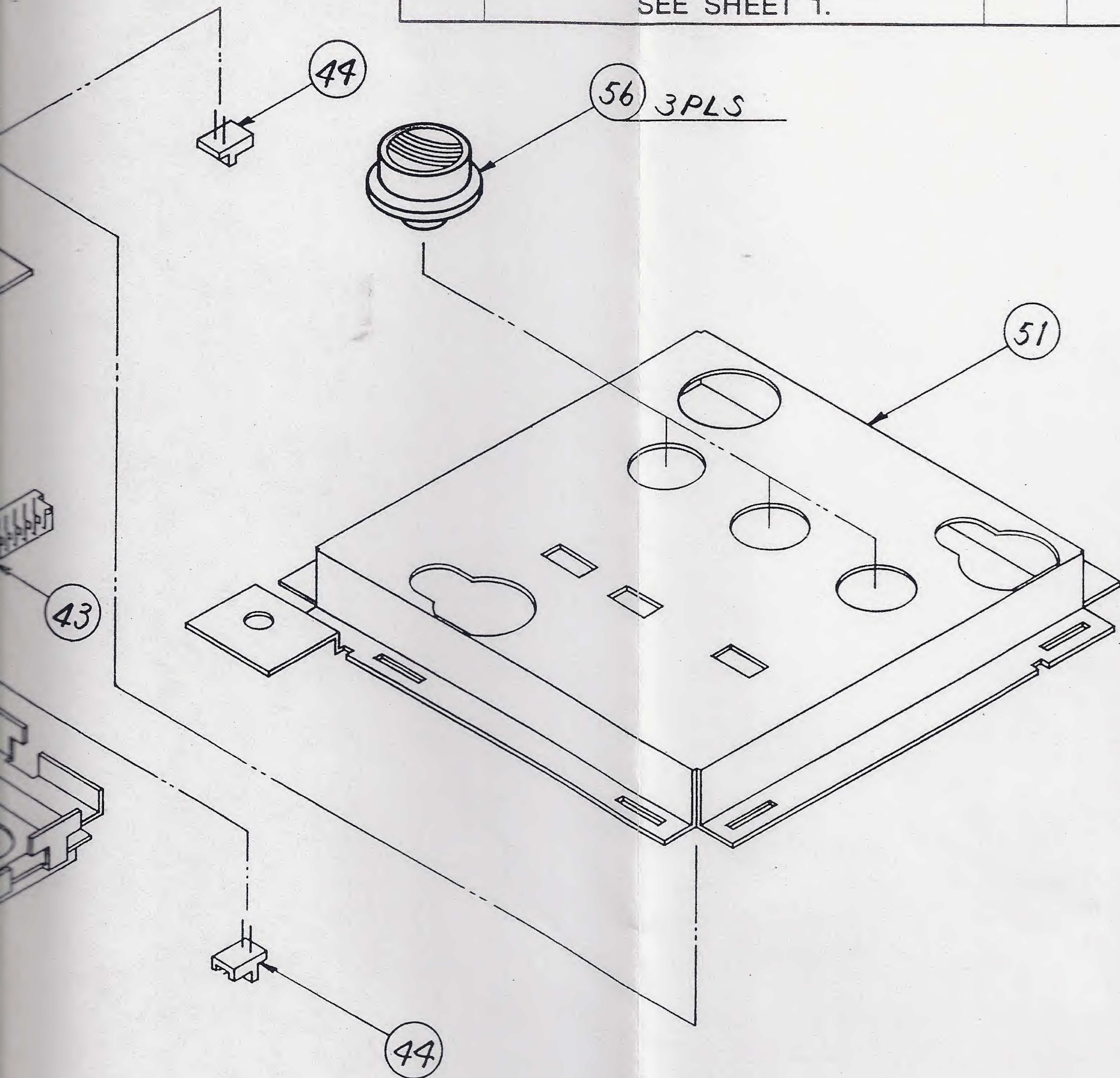


NOTES—UNLESS OTHERWISE SPECIFIED:

NEXT ASSY

APPLI

REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		



UNLESS OTHERWISE SPECIFIED TOLERANCES		
ON LINEAR DIMENSIONS		ON ANGULAR DIMENSIONS
UNDER .30	± 0.1	± 0.5°
.30 THRU .300	± 0.2	
OVER .300	± 0.4	
MATERIAL		
FINISH		

DRAWN BY	DATE
<i>E. Kuramochi</i>	5-8-89
CHECKED	DATE
<i>T. Tsuchida</i>	5-13-89
ENGINEER	DATE
<i>J. Kobayashi</i>	5-18-89
APPROVED	DATE
<i>[Signature]</i>	5/19/89
APPROVED	DATE
<i>A. Shinagawa</i>	5-19-89

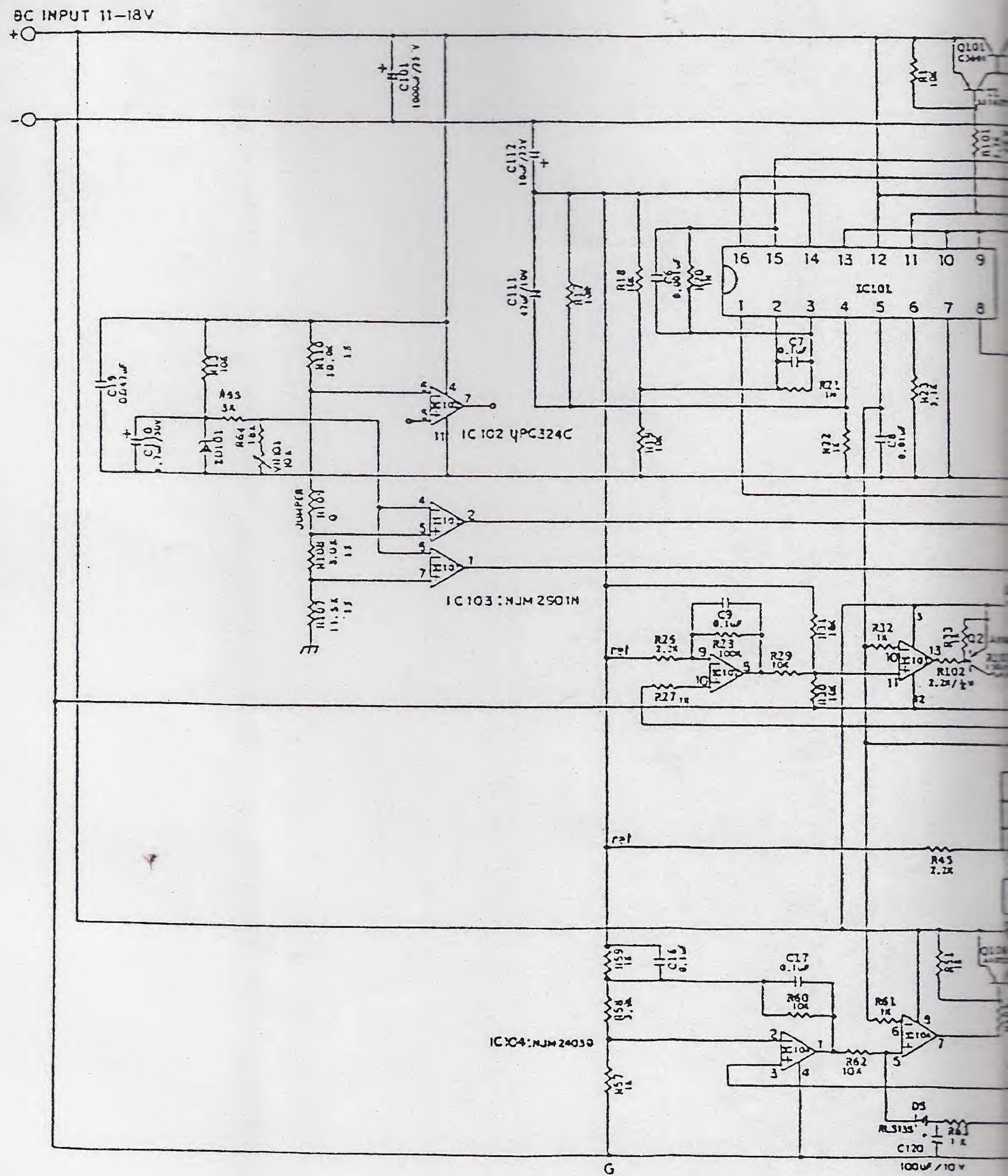


ATARI CORPORATION

TITLE		
ASSY LED PCB LST		
SIZE	DRAWING NO.	REV
B	CA200478-XXX	B
SCALE	SHEET 5 OF 5	
NONE		

USED ON ORIGINAL MODEL
LOCATION

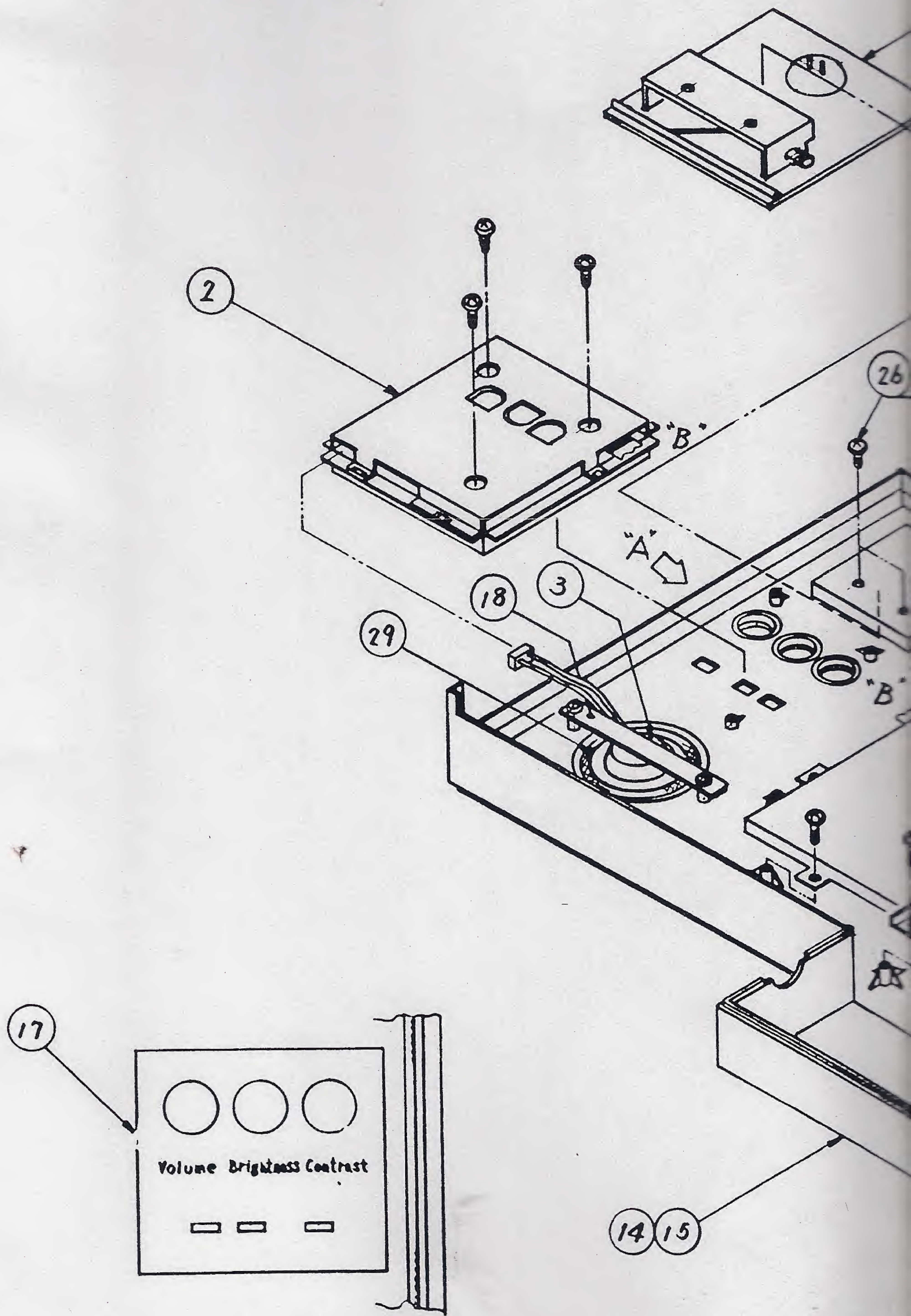
SCHEMATIC DIAGRAM



NOTES—UNLESS OTHERWISE SPECIFIED:

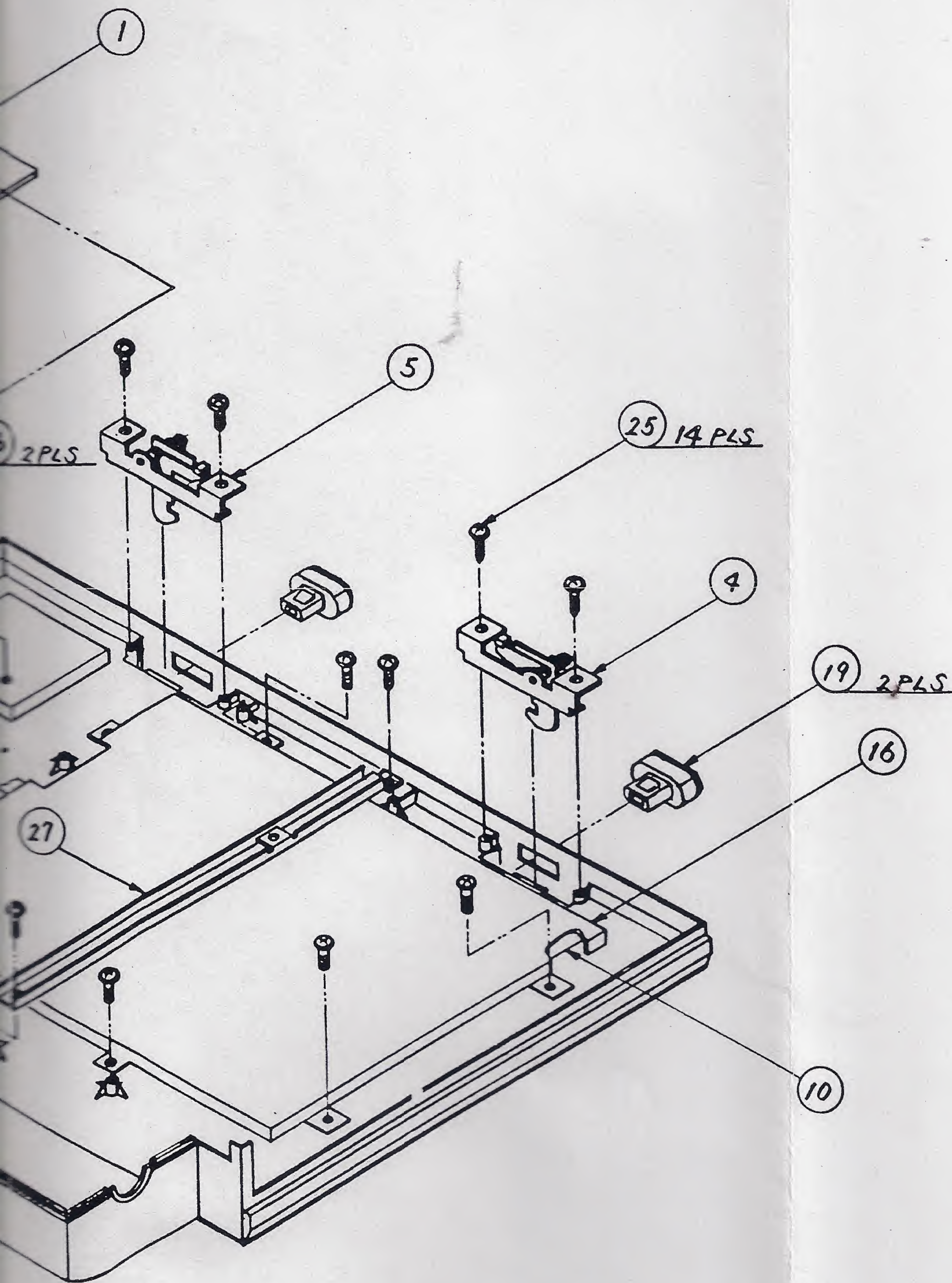
NEXT ASSY

APPLICA



VIEW A

NOTES—UNLESS OTHERWISE SPECIFIED:



REV	REVISIONS DESCRIPTION	DATE	APPROVED
	SEE SHEET 1.		

UNLESS OTHERWISE SPECIFIED TOLERANCES		DRAWN BY <i>K. Tanaka</i> 5-8-89		DATE 5-8-89	
ON LINEAR DIMENSIONS		ON ANGULAR DIMENSIONS		CHECKED <i>T. Tsuchida</i> 5-13-89	
UNDER 30 ±		±		ENGINEER <i>T. Tsuchida</i> 5-16-89	
30 THRU 300 ±		±		APPROVED <i>[Signature]</i> 5-18-89	
OVER 300 ±		±		DATE 5-18-89	
MATERIAL		FINISH		APPROVED <i>G. Shingano</i> 5-19-89	
DATE 5-19-89		DATE 5-19-89		DATE 5-19-89	
NEXT ASBY		USED ON ORIGINAL MODEL		APPLICATION	

ATARI CORPORATION		TITLE ASSY LCD PANEL LST		REV C	
SIZE C		DRAWING NO. CA200475-XXX		SHEET 4 OF 4	
SCALE NONE		THIRD ANGLE SYSTEM DIMENSION			